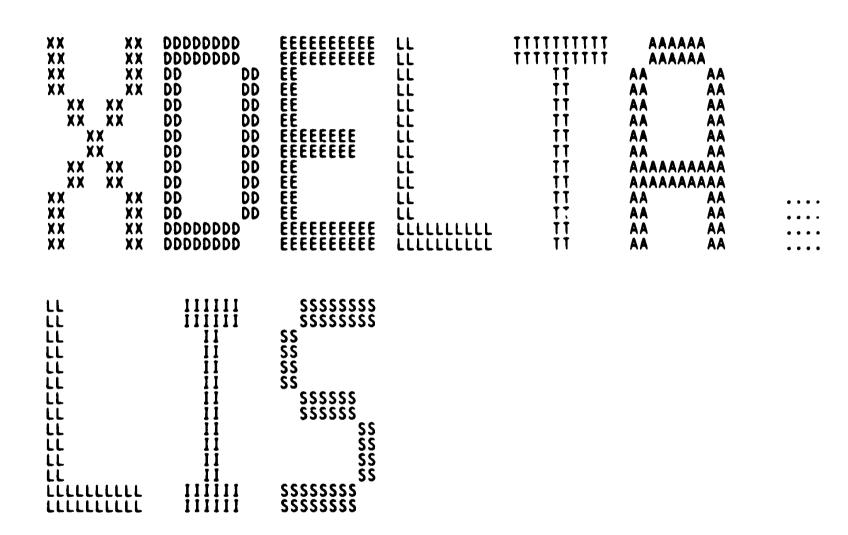
MMM	MMM	PPPPPPPPPPP)
MMM	MMM	PPPPPP PPPP)
MMM	MMM	PPPPPPPPPPP	•
MMMMMM	MMMMMM	PPP	PFF
MMMM	MMMMMM	PPP	PPF
MMMMMM	MMMMMM	PPP	PPF
MMM MM		PPP	PPF
MMM MM		PPP	PPF
MMM MMI		PPP	PPP
MMM	MMM	PPPPPPPPPPP	
MMM	1.7	PPPPPPPPPPP	
	MMM		
MMM	MMM	PPPPPPPPPPPP	,
MMM	MMM	PPP	
MMM			
	MMM	PPP	
MMM	MMM	PPP	



XD

V0

XDELTA

(1)

1474

```
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      (1)
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      (1)
                                       PRIMARY COMMAND SCANNER
                     400
429
460
477
      (1)
                                       ENDEXPR - END EXPRESSION
                                       SLASH - OPEN CELL
RETURN - CLOSE CURRENT OPEN CELL
ENDFIELD - TERMINATE CURRENT FIELD
      (1)
      (1)
      (1)
                                       FETCH - OBTAIN DATA SPECIFIED NEXTDOT - INCREMENT CURRENT LOCATION
                     498
      (1)
      (1)
                     540
554
560
587
687
779
      (1)
                                       OUTPUT - DISPLAY CONTENT
                                      LINE FEED - DISPLAY NEXT
OUTPUTA - OUTPUT ADDRESS
GETCHAR - GET INPUT CHARACTER ROUTINE
PLUS/MINUS OPERATORS
      (1)
      (1)
      (1)
      (1)
                                       TAB - INDIRECT DISPLAY
EQUALS - DISPLAY VALUE
SEMI - SECONDARY COMMAND SET
LEFT BRACKET - MODE SELECTION
      (1)
                    800
822
853
877
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      (1)
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BRKPOINT - SET/CLEAR BREAKPOINTS
GO - START EXECUTION AT SPECIFIED LOCATION
      (1)
      (1)
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                     962
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      (1)
                   1041
                                       GET SCB ADDRESS
BPT TRAP HANDLER
TBIT EXCEPTION HANDLER
      (1)
                   1166
      (1)
                   1187
                   1257
1291
1315
1344
1355
      (1)
      (1)
                                       UNBRK - RESTORE OPCODES FOR BREAKPOINTS
      (1)
                                       SETBRK - SET BREAK POINT INSTRUCTIONS
                                       GETBPTX - GET INDEX FOR BREAKPOINT QUOTE - INPUT CHARACTER STRING
      (1)
      (1)
      (1)
                   1369
                                       DEPOSIT
                                       EXECUTE - PERFORM COMMAND STRING
      (1)
                   1454
      (1)
                                       P - PROCESSOR REGISTER PREFIX
                   1466
```

PROCESS DEBUGGER INITIALIZATION

XDELTA

V04-000

```
(1)
```

```
0000
                                'V04-000'
               Version:
0000
0000
0000
                      .MCALL MEPR
                      . IF
0000
                                DF.SW PROCESS
                      TITLE
0000
                                DELTA - MULTIMODE PROCESS DEBUGGER
0000
                      .IFF
0000
                      .TITLE XDELTA - EXECUTIVE DEBUGGER
0000
                      .ENDC
0000
                      .IDENT 'V04-000'
0000
0000
0000
0000
                 COPYRIGHT (c) 1978, 1980, 1982, 1984 BY DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS.
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         28
29
0000
0000
0000
0000
0000
0000
            ; FACILITY: EXECUTIVE, DEBUGGING TOOLS
0000
0000
0000
                      THIS MODULE PRODUCES TWO DIFFERENT DEBUGGERS DEPENDING ON THE SETTING OF THE ASSEMBLY SWITCH, SW PROCESS. DELTA IS A MULTIMODE PROCESS
         37
0000
                      DEBUGGER USING SYSTEM SERVICES WHILE XDELTA IS A STANDALONE EXEC
0000
0000
         39
                      DEBUGGING TOOL.
0000
         40
0000
                      COMMAND SYNTAX IS IDENTICAL FOR BOTH VERSIONS EXCEPT FOR ENVIRONMENTAL DIFFERENCES. THE SYNTAX IS QUITE TERSE AND SOMEWHAT CRYPTIC AND
         41
0000
0000
                      IS DOCUMENTED IN THE "GUIDE TO WRITING AN I/O DRIVER".
0000
0000
         45
              ENVIRONMENT:
```

DELTA - NORMAL PROCESS ENVIRONMENT, VARIOUS ACCESS MODES.

XDELTA - STANDALONE, RESIDENT, KERNEL MODE, IPL=31

BOTH VERSIONS MUST BE POSITION INDEPENDENT - BEWARE!

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Page

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; DETAILED

Make changes to run with multi-processor privileged program.

0000 .SBTTL HISTORY 55555555566666666 0000 0000 AUTHOR: R. HUSTVEDT CREATION DATE: 15-NOV-76 0000 **REVISION HISTORY:** LJK0030 Lawrence J. Kenah 31-Jul-1981 Make changes necessary to support large physical memory configurations. Change names of PFN listhead cells. Add labels to cells for XE and XF stored strings to allow access from INIT. V02-009 LJK0030 TCM0001 Trudy C. Matthews Change all "7ZZ"s to "730"s. V02-008 TCM0001 29-Jul-1981 V02-007 KDM0003 Kathleen D. Morse 15-Sep-1980

C 6

Page 3 (1)

```
0000
                                          .SBTTL DECLARATIONS
                          72
73
74
75
76
77
               0000
                              INCLUDE FILES:
               0000
               0000
               0000
                                                                                        ; DEFINE AST CONTROL BLOCK
                                          SACBDEF
               ŎŎŎŎ
                                          $CADEF
                                                                                           DEFINE ASSEMBLY SWITCHES
               0000
                                          $CLIDEF
                                                                                           DEFINE CLI VALUES
               0000
                                                                                        ; DEFINE IPL VALUES
                                         $IPLDEF
               ŎŎŎŎ
                                                                                        ; DEFINE IRP VALUES
                                         $IRPDEF
                          80
81
82
83
                                                                                    DEFINE PROCESS CONTROL BLOCK
DEFINE PROCESSOR REGISTERS
DEFINE PRIORITY INCREMENT CLASSES
DEFINE PROTECTION VALUES
DEFINE PSL FIELDS
DEFINE SYSTEM SERVICE STATUS CODES
               0000
                                          $PCBDEF
               ŎŎŎŎ
                                          $PRDEF
               ŎŎŎŎ
                                          $PRIDEF
               0000
                                         SPRTDEF
               0000
                                         $PSLDEF
                          85
              0000
                                          $SSDEF
              0000
              0000
                              ; MACROS:
              0000
              0000
              0000
                          90
              0000
              0000
                                CPU TYPE DISPATCH MACRO:
              0000
                                         THE ADDRESSES IN THE ADDRESS LIST ARE:

-ADDRESS OF CODE FOR CPU TYPE=1 (11/780)

-ADDRESS OF CODE FOR CPU TYPE=2 (11/750)

-ADDRESS OF CODE FOR CPU TYPE=3 (11/730)

-ADDRESS OF CODE FOR CPU TYPE=4 (?)
              0000
              0000
              0000
              0000
              0000
              0000
                                                      -ETC.
              0000
                                         CPUDISP IN INVOKED TO HANDLE CPU DIFFERENCES IN LINE. WHEN THE NEXT CPU IS ADDED, ALL OCCURRENCES OF CPUDISP MUST BE EXPANDED TO HANDLE FOUR CPU SPECIFIC PATHS.
              0000
              0000
              0000
              0000
                        104
              0000
                        105
                                          .MACRO CPUDISP, ADDRLIST
                                                     GAEXESGB_CPUTYPE, <ADDRLIST>, LIMIT=#PRS_SID_TYP780, TYPE=B
              0000
                                          CASE
              0000
                        107
                                          .ENDM
                                                     CPUDISP
              0000
                        108
              0000
                        109
              0000
                              : EQUATED SYMBOLS:
                        110
              0000
                        111
                        112 V_F1=8
113 V_F2=9
114 V_F3=10
                                                                                        ; FIELD 1 PRESENT FLAG
8000000
              0000
                                                                                       ; FIELD 2 PRESENT FLAG
; FIELD 3 PRESENT FLAG
00000009
              0000
A000000
              0000
                                                                                      FIELD 4 PRESENT FLAG
FIELD 5 PRESENT FLAG
8000000B
              0000
                        115 V_F4=11
0000000C
              0000
                        116 V_F5=12
              0000
                        117
0000000
              0000
                        118 V_OPEN=0
                                                                                       ; OPEN CELL FLAG
                        119 V_ASCII=1
120 V_INFIELD=2
121 V_TBIT=3
122 V_ATBRK=4
123 V_TBITOK=5
                                                                                        : ASCII
00000001
              0000
00000002
              0000
                                                                                          FIELD IN PROGRESS
                                                                                       : FIELD IN PR
              0000
00000004
              0000
                                                                                          AT BREAKPOINT
0000005
              0000
                                                                                          TBIT EXPECTED
                        124 V_RUB=6
00000006
              0000
                                                                                       ; RUBOUT IN PROGRESS
                        125 V_NEGATE=7
                                                                                       ; NEGATE BIT
00000007
              0000
                                                                                        : PROCESSOR REGISTER MCDE
0000000F
              0000
                        126 V_PRMODE=15
```

D 6

: REGISTER SAVE AREA : RO

180 ;

182 SAVREG:

181

REGISTER SAVE AREA

.BLKL 1

0070

0070

0070

0070

00000074

XDELTA

V04-000

5 (1)

F 6

- EXECUTIVE DEBUGGER

(1)

```
0124
0124
0130
00000130
                                                                            X REGISTER VECTOR
BASE OF LOADABLE
           0130
0134
0138
00000000
                                                                             CPU DEPENDENT CODE
                                             SCHSGL_CURPCB
SCHSGL_PCBVEC
NDF,SW_PROCESS
PFNSAW_SWPVBN
PFNSAL_PTE
PFNSAL_BAK
PFNSAW_REFCNT
DENSAW_REFCNT
                                                                             X4 = CURRENT PCB ADDRESS
00000000
                                                                             X5 = BASE OF PCB VECTOR
00000000
                                                                             X6 = SWAP VBN
X7 = PTE BACK POINTER
00000000' 0140
00000000 0144
                                                                           : X8 = BACKUP ADDRESS
00000000' 0148
                                                                           ; x9 = REFERENCE COUNT
00000000
            014C
0150
                                             PFNSAX_FLINK
PFNSAX_BLINK
                                                                           ; XA = FORWARD LINK
00000000
                                                                           ; XB = BACK LINK
00000000
            0154
                                             PFN$AB_STATE
                                                                           ; XC = STATE
            0158
00000000
                                             PFNSAB_TYPE
                                                                           : XD = TYPE
            015C
 0000000
            015C
                                             XDS$GT_WORD_PFN
                                                                           ; XE: E WITH XO = PFN , DEFAULT TO WORD ARRAY
            0160
00000000
            0160
                                             XDS$GT_WORD_PFN
                                                                           ; XF; E WITH RO = PFN , DEFAULT TO WORD ARRAY
00000168
            0164
                     260
                         MCHKSAV: .BLKL
                                                                             SAVED CONTENT OF MACHINE CHECK VECTOR
            0168
                     261
                                    .IFF
                                                                             FOR PROCESS VERSION
                    262 .BLKL
263 TTIOSB: .BLKL
            0168
                                             10
            0168
                                                                             IO STATUS BLOCK FOR TERMINAL READ
            0168
                     264 TTCHAN: .BLKL
                                                                             CHANNEL NUMBER
                     265 TTNAMD: .LONG
            0168
                                              2,TTSTR
                                                                             ACTUAL ADDRESS FOR DESCR SET BY INIT
            0168
                     266 TTSTR:
                                             /††/
                                   .ASCII
            0168
                     267 DBGACTIVE:
                                                                             ACTIVE FLAGS BY ACCESS MODE
            0168
                     268
                                   .LONG
                    269 EXITBLK:
270 .LONG
271 EXIHADR: LONG
272 .LONG
273 EXCODA: LONG
274 EXITCODE:
275 .LONG
            0168
                                                                             EXIT HANDLER BLOCK
            0168
            0168
                                             EXIHANDLE
                                                                             EXIT HANDLER
            0168
                                                                             ARGUMENT COUNT
            0168
                                             EXITCODE
                                                                             ADDRESS TO STORE EXIT CODE
            0168
            0168
                                                                             RECEIVER FOR EXIT CODE
                    276 KCOND: .L
277 ECOND: .L
278 SCOND: .L
279 TERMASKD:
280 .L
            0168
                                   .LONG
                                                                             PREVIOUS KERNEL HANDLER
            0168
                                   .LONG
                                             0
                                                                             PREVIOUS EXEC HANDLER
            0168
                                    .LONG
                                                                             PREVIOUS SUPER HANDLER
            0168
                                                                             TERMINATOR MASK DESCRIPTOR
            0168
                                    .LONG
                                             16
                                                                             MASK LENGTH
                                             TERMASK
            0168
                     281
                                    .LONG
                                                                             MASK ADDRESS
                    Z82 TERMASK: LONG
            0168
                                             <1a9>!<1a10>!<1a13>!<1a2
                                                                             TAB.LF.CR.ESC
DOUBLE QUOTÉ, SLASH, EQUALS
            0168
                                             <102>!<1015>!<1029>
                    284
285
286
287
            0168
                                    .LONG
                                             <1019>
            0168
                                    .LONG
            0168
            0168
                                   .ENDC
```

0192 0192

0000002A

; NUMBER OF PRIMARY COMMANDS

NPRIM=.-PRIMARY

H 6

16-SEP-1984 02:02:16 VAX/VMS Macro V04-00 5-SEP-1984 02:07:42 [MP.SRC]XDELTA.MAR;1

					0192 0192 0192 0192 0192 0192 0192 0192	8 30 31 32	.SBTTL PRIMARY	PRIMARY COMMAND SCANNER COMMAND SCANNER	
00 00	O 0A 3	F 48	3 45 (0000	0192 3 0192 3 0194 3 0194 3 019C 3	55 55 OUTER: 56 57 DCOM: 58	.ASCIZ .WORD .IF MOVAB	<pre><lf><cr>/EH?/<lf><cr> DF,SW_PROCESS W^DBGEXCEP,(FP)</cr></lf></cr></lf></pre>	; CALL ENTRY POINT ; FOR PROCESS VERSION ONLY ; SET CONDITION HANDLER ADDRESS
AE	54 59 8 AF 50	5E	13 1 AF 01 D9 15 AB 02 O2 F C 02 O6 DF 50	11 930 930 11 33A 333 C	0183 0185 0185 0186 0186 0187 0103 0103 0103 0103 0103 0103 0103 010	8 0 1 2 3 4 5 6 7 8 9 0 1	ENDC BRBAB BSBW :MOVAB CLRB BSBB BSCC BSCASE	SCANP OUTER,R4 OUTZSTRING FP,SP INBUF-B(R11),R9 (R9) RESET NEXTP SCANP GETCHAR R8,#NPRIM,PRIMARY ERROR R0,#NPRIM,R0 R0,LIMIT=#16,<-	ENTER SCANP SET ADDR OF CONTROL STRING OUTPUT ASCIZ STRING RESET STACK RESET STACK RESET STRING ADDRESS AND FORCE READ RESET SCANNER SCAN INPUT SCAN IT ALL PROCESS NEXT PRIMARY CHAR GET CHARACTER CHECK IT NOT FOUND, ERROR RATIONALIZE INDEX DOT - CURRENT LOCATION COMMA - FIELD SEPARATOR PLUS - ADD OPERATOR BLANK - ADD OPERATOR BLANK - ADD OPERATOR * - MULTIPLY OPERATOR * - MULTIPLY OPERATOR * - MULTIPLY OPERATOR ILEFT BRACKET - MODE SELECT TAB - INDIRECT LINE FEED - NEXT LOCATION RETURN - CLOSE OPEN CELL SLASH - OPEN FOR DISPLAY DOUBLE QUOTE - OPEN FOR ASCII DISPLAY EQUALS - DISPLAY VALUE ESCAPE - PREVIOUS LOCATION 'S' - SINGLE STEP SEMI COLON - SECONDARY COMMAND COLON - SEPARATE PID FROM ADDRESS 'P' - PROCESSOR REGISTER 'Q' - LAST QUANTITY QUOTE - BEGIN ASCII STRING G - GLOBAL PREFIX H - P1 SPACE PREFIX X REGISTER
	19'	10	50	AF	0103 38 0103 0107	30000 \$:	CASEW	RO.#16.5^#<<30001\$-30000\$	

ENDEXPR

ENDEXPR

R4,R6

INFLD

END EXPRESSION

; SET INTO ACCUM ; RETURN THROUGH INFLD

BSBB

MOVL

BRB

PUSHAB

10

D0

9F

06

54

E7 AF

56

J 6

9 (1)

ŸČ

Page

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				ENUE	XPH -	END E	XAKE2210	N		3-2Fb-1884 ()2:07:42 LMP.SRCJXDELTA
					021F 021F 021F 021F	400 401 402 403	:	.SBTTL	ENDEXPR	- END EXPRESS	ON
	03	6A 56 FF	07 56 06 56 AB	E5 CE 10 04 94	021F 021F 021F 0223 0226 0228 022A	405 406 407 408 411 411	ENDEXPR	BBCC MNEGL BSBB CLRL CLRB RSB	R67R6 10\$ R6 OPER-B(SKIP IF NOT NEGATE NEGATE ACCUMULATOR PERFORM OPERATION CLEAR ACCUMULATOR INIT OPERATOR AND RETURN
04'	00	FF	AB	8f 0017' 0017'	022E 022E 022E 022E 022E 022E 022E 0233	412 413 414 415 416 417 418	10\$: 30002\$:	CASE	ADD,- ADD,- SHFT,- MUL,- DIV,- > OPER-B(R11), TYPE=B,<- R11), #0, S^#<<3(ADD-30002\$ ADD-30002\$	DO OPERATION ADD, PLUS BLANK, PLUS SHIFT, a MULTIPLY, * DIVIDE, * 0003\$-30002\$>/2>-1
				000A' 000F' 0013'	0237 0239 0238		700076	.SIGNED .SIGNED .SIGNED .SIGNED .SIGNED	WORD WORD WORD	SHFT-30002\$ MUL-30002\$ DIV-30002\$	
5	7	57	56	78	023D	419	30003 \$: SHFT:	ASHL	R6,R7,R	7	; SHIFT
		57	56	05 C4	0241 0242	420 421	MUL:	RSB Mull	R6,R7	; MULTIPLY	; AND EXIT
		57	56	05 C6	0245 0246	422 423	DIV:	RSB DIVL	R6,R7	: DIVIDE	; AND EXIT
		57	56	05 C0	0249 024 A	424 425	ADD:	RSB ADDL	R6,R7	; ADD	; AND EXIT
		.	,,	ÕŠ	024D 024E	426 427		RSB	,	, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	; AND EXIT

- EXECUTIVE DEBUGGER SLASH - OPEN CELL

L 6	16-SEP-1984	02:02:16	VAX/VMS Macro VO4-00
	5-SEP-1984	02:07:42	VAX/VMS Macro V04-00 [MP.SRC]XDELTA.MAR;1

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					024E 024E	429 430	.SBTTL	SLASH - OPEN CELL	
					024E 024E 024E 024E 024E	430 431 : 432 : 433 : 434 DQUO' 435	OPEN SP	ECIFIED CELL	
		6 A	02 03	88 11	024E 024E 0251	434 ĎQUO' 435 436 437	TE: BISB BRB	#<1av_ASCII>,(R10) OPEN	DISPLAY IN ASCII SET ASCII FLAG
	06	6A 6A	02 46 08	8A 10 E0 D0	02533 02533 02558 02560 02560	438 SLASI 439 440 OPEN 441	BICB BSBB BBS	#<1av ascii>,(R10) ENDFIELD #V_F1,(R10),5\$	CLEAR ASCII DISPLAY MODE TERMINATE FIELD ADDR SPECIFIED?
50 6A	6B 6A 01	04 E0 01 1F	AB 04 AB 0f 50	00	UZDZ	442 443 444 5\$: 445 10\$:	MOVL BRB MOVL EXTZV	10\$: AND DISPLAY CONTENT : SET NEW DOT : GET PROCESSOR REGISTER MODE FLAG
UA .	_		086 09 AB 14	EF F0 30 E1 D1	0266 026B 0270 0273 0277 0278	446 447 448 449 15\$:	ÍNSV BSBW BBC CMPL BLEQ	#V_PRMODE,#1,(R10),R0 RO,#V_PREG,#1,(R10) LOCOUT #V_F2,(R10),RSET F2=B(R11),CURDOT-B(R11) RSET	AND MOVE TO SEMI-PERMANENT COPY OUTPUT AND OPEN RANGE SPECIFIED? CHECK FOR END YES
			76	10	027D 027D 027F	450 451 452 453	.IF BSBB .IFF	NDF, SW_PROCESS NEXTLOC	INCREMENT TO NEXT DOT
		£ f	F6	11 31	027F 027F 027F 0281	454 455 456 457 ERR4:	BSBW .ENDC BRB	NEXTLOC 15\$ ERROR	INCREMENT TO NEXT DOT AND CONTINUE DECLARE ERROR
		• •	10	٠, ر	0284	458	. UNW	L N N O N	PECLANE ENNOR

	- EXECUTIVE DEBUGGER RETURN - CLOSE CURRENT	M 6 OPEN CELL 16-SEP-19 5-SEP-19	84 02:02:16 VAX/VMS Macro V04-00 84 02:07:42 [MP.SRC]XDELTA.MAR;1	Page 12 (1)
	0284 460 0284 461 0284 462 : 0284 463 ; 0284 464 : 0284 465 0284 465 RETURN	.SBTTL RETURN - CLOSE CUR RETURN - CLOSE CURRENT OPE		
18 0A 6A 00 03 6A 08 0560 01E3 F9 6A 08 01D4	0284 466 RETURN 10 0284 467 0286 468 E5 0286 469 E1 028A 470 30 028E 471 31 0291 472 RSET: E1 0294 473 10\$: 31 0298 474 0298 475	BSBB ENDFIELD .ENABL LSB BBCC WV_OPEN,(R10),10\$ BBC WV_F1,(R10),RSET BSBW DEPOSIT BRW RESET BBC WV_F1,(R10),RSET BRW EQE1 .DSABL LSB	TERMINATE CURRENT FIELD SKIP IF NONE OPEN SKIP IF NOTHING TO STORE DEPOSIT RESET SCANNER DONE IF NO INPUT OTHERWISE OUTPUT	

XDELTA VO4-000

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R6

INCB CLRQ

RSB

495 10\$:

N 6

					02B9 498 02B9 499		.SBTTL	FETCH -	OBTAIN (DATA SPECI	FIED
					02B9 500 02B9 501	:	FETCH S	PECIFIED	DATA		
	10	6 A	1F	EO	02B9 502 02B9 503 02B9 504 02BD 505 02BD 506 02BD 507 02BD 508	FETCH:	BBS .IF	#V_PREG	,(R10),4(ROCESS) \$	BR IF PROCESSOR REGISTER
					02BD 505 02BD 506 02BD 507 02BD 508		TSTL BNEQ	PID-BTR 50\$,(R10),4(ROCESS 11)		CHECK FOR PROCESS GET BR IF YES
					02BD 508 02BD 509 02BD 510 02BD 511 02BD 512 02BD 513 02BD 513 02C2 02C2 02C4 02C6		.ENDC CASE	CURTYPE- 10\$,- 20\$,- 30\$,-	-B(R11),1	'YPE=8,<-	; OPERATE ON TYPE BYTE WORD LONG
02'	00	FE	AB	8F	02BD	30004\$:	CASEB	CURTYPE	-B(R11),	0,5^#<<3b	005\$-30004\$>/2>-1
				0006' 000C' 0012'	0202 0202 0204 0206		.SIGNED .SIGNED .SIGNED	_WORD _WORD _WORD	10\$-3000 20\$-3000 30\$-3000	04\$	
04	AB	00	88	9A	02C8 02C8 514 02CD 515 02CE 516 02D3 517	30005 \$: 10 \$:	MOVZBL			DUAN-B(R11	; GET BYTE
04	AB	00	88	05 30	02CD 515 02CE 516 02D3 517	20\$:	RSB MOVZWL	acurdot-	-B(R11),	QUAN-B(R1	RETURN DETURN
04	AB	00	88	05 00 05	0203 517 0204 518 0209 519	30\$:	RSB Movl RSB			DUAN-B(R1	RETURN
	04	AB	5B	DB	02DA 520 02DA 521 02DA	40\$:	.IF MFPR	NDF,SW_I CURDOT=I MFPR	PROCESS B(R11),Ql CURDOT-E	: JAN-B(R11 R11), QUA	GET PROCESSOR REGISTER
				05	02DE 522 02DF 523 02DF 524 02DF 525	400	RSB .IFF				FALSE IF PROCESS VERSION
					02DF 523 02DF 524 02DF 525 02DF 526 02DF 527	40\$:	\$CMKRNL	_\$	B^FTCHPF	REG,(AF)	CALL IN KERNEL MODE TO FETCH
					0201 528	50\$:	RSB BRW .ENDC	FETCHP		;	FETCH FROM FOREIGN PROCESS
					02DF 530 02DF 531	FTCHPRE	.IF G:	DF,SW_PI	ROCESS	•	
					02DF 532 02DF 533 02DF 534 02DF 535 02DF 536		.WORD MOVAB MFPR MOVL RET	O W^PREXC CURDOT-I #1,RO	,(FP) B(R11),QL	JAN-B(R11)	ENTRY MASK SET EXCEPTION HANDLER ; GET PROCESSOR REGISTER RETURN SUCCESS
					02DF 537 02DF 538		.ENDC			;	

B 7

15 (1)

C 7

ASSUME UNIT INCREMENT CHECK FOR PREG YES, USE UNIT INCREMENT FORM INCREMENT AND ADD TO DOT RETURN MOVL TSTL BLSS ROTL #1 R1 (R10) 10\$_ 51 DO D5 19 9C CO O5 CURTYPE-B(R11),R1,R1 R1,CURDOT-B(R11) FE AB 51 51 51 6B ADDL RSB

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```
554
555;
556;
557;
558 OUTBB:
559
                                                          .SBTTL OUTPUT - DISPLAY CONTENT
                                                          OUTPUT CONTENT
                                                          .BYTE
             10 00 04
                                                          .BYTE 4,12,28
.SBTTL LINE FEED - DISPLAY NEXT
                                                                                                           ; STARTING DIGIT LIST
                                        561 :
562 :
563 :
                                       564 LINEFEED:
565 B:
566 NEXTLOC:
567 B:
            FF8F
                       30
                                                          BSBW
                                                                      RETURN
                                                                                                              CLOSE OPEN CELL
                                                                                                              PROMPT WITH NEXT LOCATION
                       10
               E8
                                                          BSBB
                                                                      NEXTDOT
                                                                                                              INCREMENT LOCATION
                                        568 LOCPROMPT:
                                                                                                              DISPLAY ADDR/CONTENT
                                       568 LOCPROMPT:
569 BSBB
570 LOCOUT: BSBB
571 BISL
572
573 OUTPUT:
574 MOVZB
575 MOVZB
576 MOVL
577 BBS
               2B
BE
01
                       10
10
08
                                                                      OUTPUTA
                                                                                                              OUTPUT ADDRESS
                                                                                                           FETCH CONTENT
                                                                      FETCH
       6A
                                                                      #<1av_OPEN>,(R10)
                                                                                                           ; INDICATE OPEN CELL
51 FE AB
52 E9 AF41
53 04 AB
04 6A 01
                       9A
9A
DO
EO
10
                                                          MOVZBL
                                                                      CURTYPE-B(R11),R1
OUTBB[R1],R2
QUAN-B(R11),R3
                                                                                                              GET TYPE INIT DIGIT SELECTOR
                                                          MOVZBL
                             0307
                                                                                                              GET QUANTITY TO DISPLAY
                             030B
                                                                      WV ASCII, (R10), 10$
OUTCOM
                                                                                                              CHECK FOR ASCII OUT
                                        578
579
                             030F
                                                                                                             OUTPUT NUMBER IN HEX
AND EXIT THROUGH OUTSPACE
PUT STRING IN BUFFER
GET COUNT
                                                          BSBB
               ÓÉ
53
51
                             0311
                                                                      20$
R3,OUTBUF-B(R11)
R1,#1,R2
                       11
                                                          BRB
                       DO 78 94 10
                                       580 10$:
581
   08 AB
                                                          MOVL
       01
                             0317
                                                          ASHL
                             031B
031F
            AB42
59
                                        582
       80
                                                          CLRB
                                                                      OUTBUF-B(R11)[R2]
                                                                                                             MARK END OF STRING
OUTBUT ASCIIZ BUFFER
                                        583
                                                          BSBB
                                                                      OUTZBUF
                             0321
0324
                                       584 20$:
                       31
            008B
                                                                      OUTSPACE
                                                                                                           ; FOLLOW WITH SPACE
                                                          BRW
```

D 7

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	0324 587 .SBTTL 0324 588 : 0324 589 : OUTPUT 0324 590 : 0324 591 OUTPUTA:	OUTPUTA - OUTPUT ADDRESS ADDRESS	·
008D 30 53 18 AB 9E	0324 592 BSBW 0327 593 MOVAB 032B 594 .IF 032B 595 TSTL 032B 596 BNEQ	CRLF SAVREG-B(R11),R3 DF,SW_PROCESS PID-B(R11) 3\$; OUTPUT ADDRESS ; OUTPUT CR/LF ; BASE OF REGISTER ; ONLY FOR PROCESS ; CHECK FOR OTHER P	AREA VERSION PROCESS ADDRESS
53 6B 53 C3 12 19 53 04 C6 0F 53 D1 0A 14 50 52 8F 9A 52 10 52 D4 13 11	0334 601 CMPL 0337 602 BGTR 0339 603 MOVZBL 033D 604 BSBB 033F 605 CLRL	R3, CURDOT-B(R11), R3 S\$ W4,R3 R3,W15 S\$ W^A'R',R0 OUTCHAR R2 COMPUTE OFFSET IN NOT GENERAL REGIS SCALE TO LONGWORD CHECK FOR MAX REG GTR, NOT A REGIST OUTPUT PREFIX OF 'R' AND SET FOR ONE D	TO REGISTER AREA TER NUMBER NUMBER ER
13 11	0341 606 BRB 0343 607 .IF 0343 608 3\$: TSTL 0343 609 BLSS 0343 610 MOVL 0343 611 MOVL 0343 612 BSBB 0343 613 MOVZBL 0343 614 BSBB 0343 615 .ENDC 0343 616 5\$: MOVL	DF,SW_PROCESS FOR PROCESS VERSI (R10) ; CHECK FOR PROCESS 5\$; BR IF YES #28,R2 ; SET FOR LONGWORD PID-B(R11),R3 ; GET PID OF TARGET OUTCOM ; OUTPUT PID AS LON	ON ONLY OR REGISTER OUTPUT
53 6B D0 52 1C D0 6A D5 09 18 50 50 8F 9A 3E 10 52 04 D0 0C 10	0349 618 TSTL 034B 619 BGEQ 034D 620 MOVZBL 0351 621 BSBB 0353 622 MOVL	CURDOT-B(R11),R3 #28,R2 (R10) 10\$ #^A'P',R0 OUTCHAR #4,R2 OUTCOM #SLSH,R0 OUTCHAR OUTCHAR #5LSH,R0 OUTCHAR PETURN THROUGH OU	OUTPUT OR REGISTER ORD GITS
50 2F 9A 34 11 52 D4 03 11	0356 623 10\$: B\$BB 0358 624 MOVZBL 035B 625 BRB 035D 626 OUTDIGIT: 035D 627 CLRL 035F 628 BRB 0361 629 0361 630 OUTLONG:	#SLSH,RO CUTPUT SLASH OUTCHAR RETURN THROUGH OU R2 CUTPUT ONE DIGIT R2 CAP DIGIT SELECTO OUTCOM AND MERGE WITH CO COMPUT LONGWORD	LILIAN
52 1C D0 54 08 AB 9E 51 53 04 52 EF 84 FDF6 CF41 90 52 04 C2 F0 18 64 94 54 08 AB 9E	0361 631 MOVL 0364 632 OUTCOM: 0364 633 MOVAB 0368 634 10\$: EXTZV 036D 635 MOVB 0373 636 SUBL 0376 637 BGEQ 0378 638 CLRB 037A 639 OUTZBUF: MOVAB	#28,R2 OUTBUF-B(R11),R4 R2,#4,R3,R1 PRIMARY[R1],(R4)+ #4,R2 10\$ (R4) OUTBUF-B(R11),R4 FORMAT IT GET ADDRESS OF OU GET DIGIT NEXT DIGIT NEXT DIGIT MARK END OF BUFFE OUTBUF-B(R11),R4 GET START OF BUFF	R
50 84 9A 04 13	037E 640 037E 641 OUTZSTRING: 037E 642 MOVZBL 0381 643 BEQL	(R4)+,R0 ; OUTPUT ASCIZ STRI GET A CHAR BR IF DONE	

E 7

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- EXECUTIVE	DEBUGGER
OUTPUTA - OL	ITPUT ADDRESS

		0 C F 7	10 11 05	0383 0385 0387 0388	647	10\$:	BSBB BRB RSB	OUTCHAR OUTZSTRING	; OUTPUT CHAR ; CONTINUE ; RETURN IF DONE
50	5C 50	8F 03 58	9A 11 9A	03857 03888 033888 033886 033881	648 649	OUTBSLS: OUTR8: OUTCHAR	MALITAL	#BSLSH,RO OUTCHAR R8,RO	OUTPUT BACK SLASH SET CHARACTER CODE AND OUTPUT IT GET CHAR TO OUTPUT OUTPUT CHAR IN RO CHECK FOR CONSOLE NO, USE DEVICE DIRECTLY GET CONSOLE TRANSMIT STATUS
	51	5 C 0 5	D5 12 DB	0391 0391 0393 0395 0395	654 655 656 657	OUTR8: OUTCHAR	IF TSTL BNEQ MFPR	NDF,SW_PROCESS AP 10\$ #PR\$_TXCS,R1 MFPR #PR\$_TXCS,R1	CHECK FOR CONSOLE NO, USE DEVICE DIRECTLY GET CONSOLE TRANSMIT STATUS
51 EF	04	04	11 B0 E1 D5 12	0398 039A 039E 03A2 03A4	658 659 660 661 662	10 \$: 20 \$:	BRB MOVW BBC TSTL BNEQ MTPR	20\$ OUTCR(AP),R1 #7,R1,OUTCHAR AP 30\$; GET CONSOLE TRANSMIT STATUS ; MERGE WITH COMMON CODE ; GET STATUS ; WAIT FOR READY ; CHECK FOR CONSOLE ; YES ; SEND CHARACTER TO CONSOLE ; RETURN ; OUTPUT CHAR ; FALSE FOR PROCESS VERSION ; BUFFER CHARACTER ON STACK ; SAVE POINTER TO IT
06	23 AC	50 50	DA 05 90	03A6 03A9 03AA	663 664 665 666	30\$:	MTPR RSB MOVB .IFF PUSHL	RO, #PR\$_TXDB RO,OUTB(AP)	SEND CHARACTER TO CONSOLE RETURN OUTPUT CHAR FALSE FOR PROCESS VERSION
				03AE 03AE 03AE 03AE 03AE	667 668 669 670 671 672 673		MOVL \$QIO_S	RO SP,RO EFN=#30,- CHAN=TTCHAN,- FUNC=#IO\$_WRITEVBLK,- P1=(RO),- P2=#1 #^M <ro> #32,RO OUTCHAR #CR,RO OUTCHAR #LF,RO OUTCHAR</ro>	; BUFFER CHARACTER ON STACK ; SAVE POINTER TO IT ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ;
			05	03AE 03AE 03AE 03AF	676	OUTSPACE	POPR .ENDC RSB E:	#^M <ro></ro>	RESTORE CHARACTER AND RETURN
	50 50 50	20 00 08 0 A	9A 11 9A 10 9A	03AF 03B2 03B4 03B7 03B9	679	CRLF:	MOVZBL BRB MOVZBL BSBB MOVZBI	#32,R0 OUTCHAR #CR,R0 OUTCHAR	; SET CODE FOR SPACE ; AND SEND IT ; RETURN ; SEND IT
	,,	03	11	03BC 03BE 03BE	683 684 685		BRB	OUTCHAR	SEND IT

F 7

(1)

FF8C

06

03 6A

30

11

E 5

03FF

0402

0404

740

741 905:

BSBW

BRB

BBCC

OUTR8

#V_RUB,(R10),100\$

ŽÕ\$

OUTPUT RUBBED OUT CHAR

TERMINATE RUBOUT SEQUENCE

AND GET ANOTHER

16-SEP-1984 02:02:16 VAX/VMS Macro V04-00 5-SEP-1984 02:07:42 [MP.SRC]XDELTA.MAR;1

03 58 06 58 20	30 0408 E1 0408 8A 040F 0412	742 743 100\$: 744 745 110\$:	BSBW BBC BICB	OUTBSLSH #6,R8,110\$ #32,R8	; OUTPUT BACK SLASH ; BR IF NOT ALPHA ; SET TO UPPER CASE
FF79	30 0412 0415	746 747 748	.IF BSBW .ENDC	NDF, SW_PROCESS OUTR8	ECHO CHARACTER
FD63 CF 08 58 A8 58 0D	90 0415 3A 0418 13 041E 91 0420	749 750 751 752	MOVB LOCC BEQL CMPB	R8,(R9)+ R8,#NTERM,TERM 20\$ #CR,R8	; BUFFER NEW CHAR ; CHECK FOR TERMINATOR ; NOT A TERMINATOR ; IS CHAR = RETURN
03 FF8C 69	12 0423 30 0425 94 0428	753 754 755 120 \$:	BNEQ BSBW CLRB	120 \$ CRLF (R9)	; NO, ; YES, SEND CR/LF ; MARK END OF BUFFER
59 AC AB FF8D	9E 042A 31 042E	756 757	MOVAB Brw	INBUF-B(R11),R9 GETCHAR	; RESTORE BUFFER BASE ; AND TRY AGAIN

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		- EX PLUS	ECUTIVE /MINUS	DEBI OPER/	JGGER ATORS		I 7 16-SEP-1984 02 5-SEP-1984 02	:02:07:	16 42	VAX/VMS Macro V04-00 [MP.SRC]XDELTA.MAR;1
			0431 0431	759 760	•	.SBTTL	PLUS/MINUS OPERATORS			
			0431	760 761 762 763	:	PLUS/MI	NUS OPERATORS			
			0431 0431	763 764	BLANK: OPERATOR	? :		; 9	SAME	AS PLUS
FF AB	50 FDEB	30 83 05	0431 0434 0439 043A	765 766 767		BSBW SUBB3 RSB	ENDEXPR #OPERBAS,RO,OPER-B(R11)	: 5	ND E SET (RETUR	OPERATOR
			043A 043A	768 769 770	:	MONADIC	MINUS - NEGATE			
6 A	56 03 FDDE 80 8F	D5 13 30 80 05	043A 043C 043E 0441 0445 0446	771 772 773 774	NEGATE: 5\$: 10\$:	TSTL BEQL BSBW XORB RSB	R6 5\$ ENDEXPR #<1@V_NEGATE>,(R10)	; E	MPT) THER TOGGL	ACCUMULATOR RWISE PERFORM OPERATION LE NEGATE FLAG RETURN

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					0446 0446 0446 0446	779 780 : 781 :	.SBTTL	TAB - INDIRECT DISPLAY
50 6A	6A 01	01 1F	4 AB 0F 50 0F	DO EF FO 11	0446 044A 044F	780 ; 781 ; 782 ; 783 † AB : 785 ; 786 ; 787 ; 788 ; 789 ; 790 ; 791	MOVL EXTZV INSV BRB	QUAN-B(R11), CURDOT-B(R11); GO INDIRECT #V_PRMODE, #1, (R10), R0; GET PROCESSOR REGISTER MODE RO.#V_PREG, #1, (R10); AND COPY TO SEMI-PERMANENT FLAG LOCP; AND DISPLAY IT
					0456 0456 0456 0456 0456	788 ; 789 ; 790 ; 791 ;	ESCAPE	- DISPLAY PREVIOUS LOCATION
51	51	51 6B	01 6A 05 E AB 51 FE8F	DO D5 19 9C C2 31	0456 0459 0458 0450 0462 0465	792 ESCAP: 793 794 795 796 797 10\$: 798 LOCP:	MOVL TSTL BLSS ROTL SUBL BRW	#1,R1 (R10) 10\$ CURTYPE-B(R11),R1,R1 R1,CURDOT-B(R11) LOCPROMPT ASSUME UNIT INCREMENT CHECK FOR PROCESSOR REGISTER YES, USE UNIT INCREMENT FORM INCREMENT AND SUBTRACT FROM DOT PROMPT WITH CONTENT

J 7

	- EXECUTIVE DEB EQUALS - DISPLA	UGGER Y VALUE	16-SEP-1984 02 5-SEP-1984 02	:02:16 VAX/VMS Macro V04-00 :07:42 [MP.SRC]XDELTA.MAR;1	Page 23 (1)
	0468 800 0468 801	.SBTTL	EQUALS - DISPLAY VALUE		
	0468 800 0468 801 0468 802 0468 803	EQUALS	- VALUE DISPLAY		
FE33 05 6A 08 04 AB E0 AB FE87	0468 801 0468 802 0468 803 0468 804 0468 805 30 0468 806 E1 046B 807 D0 046F 808 30 0474 809 0477 811 0477 811	EQUALS: .ENABL BSBW BBC .EQL1: MOVL	LSB ENDFIELD #V_F1,(R10),10\$ F1=B(R11),QUAN-B(R11) OUTPUT RESET LSB	TERMINATE FIELD IGNORE IF FIELD BLANK SET QUANTITY OUTPUT IT RESET SCANNER	
	0477 814 0477 815 0477 816	RESET			
6A OOFFFF80 8F FC AB 56	CA 0477 817 94 047E 818 7C 0481 819 05 0483 820	RESET: BICL CLRB CLRQ	#^XOFFFF80,(R10) FCTR-B(R11) R6	; CLEAR FIELD AND NEGATE (; CLEAR FIELD COUNTER ; RESET ACCUMULATORS ; RETURN	FLAGS

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```
.SBTTL SEMI - SECONDARY COMMAND SET
                           0484
                           0484
                                                  SEMI
                           0484
                           0484
                           0484
                                        SECOND:
                                                                                           X REGISTER SET/DISPLAY
                      58
50
                          0484
                                                  .ASCII
                                                           /X/
                           0485
                                                  . AŠČĪĪ
                                                                                           P - PROCEED
                                                           /P/
                      4D
                          0486
                                   830
                                                                                           M - SET MODIFY FLAG
                                                  .ASCII
                                                           /M/
                      49
                                                                                           I - PROGRAM COUNTER
                          0487
                                   831
                                                  .ASCII
                                                           /1/
                                                                                           G - GO, START
E - EXECUTE STRING
B - SET/CLR BREAKPOINT
                      47
                                   832
833
                          0488
                                                  .ASCII
                                                           /G/
                      45
                          0489
                                                           /E/
                                                  .ASCII
                           048A
                                                   ASCII
                                                           /B/
              00000007
                           048B
                                   835
                                                                                           NUMBER OF SECONDARY COMMANDS
                                       NSEC=.-SECOND
                           048B
                                   836
                                   837
                                       SEMI:
                                                           #<1av_OPEN>,(R10)
ENDFIELD
                                   838
               01
                          048B
                                                  BICB
                                                                                           CLEAR OPEN FLAG
         6A
                      30
30
3A
                                   839
                                                                                           TERMINATE FIELD
             FEOD
                          048E
                                                  BSBW
                           0491
                                                                                           GET SECONDARY COMMAND CHAR
                                   840
                                                  BSBW
                                                            GETCHAR
                                                            R8, #NSEC, SECOND
                                                                                           LOCATE SECONDARY COMMAND SWITCH ON TYPE
EB AF
         07
                58
                           0494
                                   841
                                                  LOCC
                                   842
843
                           0499
                                        105:
                                                  CASE
                                                            ROLLMIT=#1.<-
                           0499
                                                            BRKPOINT,-
                                                                                            SET BREAKPOINT
                           0499
                                   844
                                                                                           EXECUTE STRING
                                                            EXECUTE. -
                           0499
                                   845
                                                            GO,-
                                                                                            SEMI-G, GO
                                                                                           SEMI-I, INSTRUCTION CONTER
                                   846
                                                            PROGCTR.-
                           0499
                                                                                           SEMI-M, MODIFY FLAG
                           0499
                                   847
                                                            MFYFLGS .-
                                                            PROCED .-
                                                                                           SEMI-P, PROCEED
                           0499
                                   848
                                   849
                                                            XSET,-
                                                                                           SET XREGISTER
                           0499
                           0499
                                   850
   06'
         01
                50
                          0499
                                                  CASEW
                                                           RO,#1,S^#<<30011$-30010$>/2>-1
                     AF
                                        30010$:
                           049D
                                                  .SIGNED_WORD
.SIGNED_WORD
.SIGNED_WORD
.SIGNED_WORD
.SIGNED_WORD
                                                                     BRKPOINT-30010$
EXECUTE-30010$
G0-30010$
                   003A'
                          049D
                   037A'
                          049F
                   00D8'
                          04A1
                   0105' 04A3
                                                                     PROGCTR-30010$
                   00EC'
                          04A5
                                                                     MFYFLGS-30010$
                   00Ē1'
                                                                     PROCED-30010$
                          04A7
                                                  .SIGNED_WORD
                                                                     XSET-30010$
                          04A9
                                        300115:
                           04AB
                      31
                                                            ERROR
             FCFO
                                   851 ERR2:
                                                  BRW
                                                                                         : ERROR
                           04AB
```

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04. 04.	AE 853 .SBTTL L	EFT BRACKET - MODE SELECTION	
04, 04, 04,	AE 856: LEFT BRAC	KET	
04/ 43 04/ 40 04/ 57 04/ 42 04/ 0000004 04/	AE 859 .ASCII / AF 860 .ASCII / BO 861 .ASCII / B1 862 .ASCII / B2 863 NMODES=MODES	; MODE CHARACTER (; CHARACTER L/ ; LONG, HEX W/ ; WORD, HEX B/ ; BYTE, HEX ; NUMBER OF MODE (
041 041 041 041 041 041 041 041	B2 867 BSBW G B5 868 LOCC R BA 869 BEQL E BC 870 BBS W CO 871 SUBB3 W C5 872 BICB W	## CHAR ## CONVERT TO INDEX #	R
6A 02 88 040 05 040	C9 874 10\$: BISB #	<1av_ASCII>,(R10) ; SET CHARACTER MO	ODE

M 7

VC XD

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		04D7	885		.SBTTL	BRKPOINT - SET/CLEAR BREA	AKPOINTS
		04D7 04D7	887 887	BRKPOIN	BRKPOIN	T	
		04D7	888		-	•	
58 6A 08	E1	04D7 04D7	890	BKKLOIN	BBC	#V_F1,(R10),SHOBRK	DISPLAY BREAKPOINTS
58 6A 08 12 6A 09 52 01	ĒΟ	04DB	ולם		882	#V_F2,(R10),20\$ #1,R2	YES, IT WAS SPECIFIED
FBD CF42	EO DO D5 13	04DF 04E2	893	10\$:	MOVL TSTL	BRKADR[R2]	FIND FREE SLOT
FFF3 52 01 08	13 F1	04E2 04E7 04E9	894 895		BEQL ACBL	30\$ #NBRK,#1,R2,10\$	YES, GOT ONE
BA	11	04EF	896		BRB	ERR2	ERROR
52 E4 AB EB	D0 13	04F1 04F5	897 898	20\$:	MOVL Beql	F2-B(R11),R2	GET BRKPOINT NUMBER
52 08 AF	D1 19	04F7	899		CMPL	10\$ #NBRK,R2	CHECK FOR LEGAL
FBDF CF42	D4	04FA 04FC	900 901	30\$:	BLSS CLRL	ERR2 BRKDSP[R2] BRKCOM[R2] F1-B(R11),R0	DISPLAY BREAKPOINTS YES, IT WAS SPECIFIED INIT INDEX FIND FREE SLOT YES, GOT ONE CHECK THEM ALL ERROR GET BRKPOINT NUMBER NULL FIELD, SCAN FOR SLOT CHECK FOR LEGAL OUT OF RANGE CLEAR DISPLAY CLEAR COMMAND ADDRESS GET BREAKPOINT ADDRESS ALLOW CLEAR OF BREAKPOINT
FBFA CF42 50 EO AB	D4 D0	0501 0506	902 903		CLRL Movl	BRKCOM[R2];	CLEAR COMMAND ADDRESS
03	13	050A	904		BEQL	35\$	ALLOW CLEAR OF BREAKPOINT
		050C 050C	905 906		.IF PUSHR	DF,SW_PROCESS #^M <ru,r1,r2,r3,r4,r5,r6></ru,r1,r2,r3,r4,r5,r6>	: SAVE REGISTERS FOR PROTECTION CHAN
		050C 050C 050C	907 908		MOVI	RO.R5 •	SET START ADDRESS
		050C	909		BSBW	SETURT	AND END ADDRESS SET PAGE WRITABLE
		050C 050C	910 911		MOVL .ENDC	(SP),RO	RESTORE BPT ADDRESS
60 60	90	050C 050F	912 913		MOVB . I F	(RO),(RO)	TEST WRITABILITY OF ADDRESS
		050F	914		BSBW	DF,SW_PROCESS : REPROT :	RESTORE PROTECTION
		050F 050F	915 916		POPR .ENDC	#^M <r0,r1,r2,r3,r4,r5,r6></r0,r1,r2,r3,r4,r5,r6>	
OC 6A OA FBC6 CF42 E8 AB	E 1 D0	050F 0513	917 918	35\$:	BBC MOVL	#V_F3,(R10),40\$	DISPLAY SPECIFIED? SET DISPLAY START SKIP TEST IF NULL CHECK READABILITY SKIP IF NO COMMAND ADDRESS SET COMMAND STRING SAVE READROINT ADDRESS
03	13	051A	919		BEQL	40\$	SKIP TEST IF NULL
07 6A 0B	D5 E1 D0 D0	051C 051F 0523	920 921	40\$:	TSTL BBC	<pre>aF5-B(R11) #V F4.(R10).45\$</pre>	CHECK READABILITY SKIP IF NO COMMAND ADDRESS
FBD6 CF42 EC AB FB88 CF42 50	DO	0523 052A	922	150	MOVL	F4-B(R11), BRKCOMER2]	SET COMMAND STRING
FF44	31		924	473:	MOVL Brw	NO DUNNUNCUEJ	SAVE BREAKPOINT ADDRESS RESET SCANNER AND RETURN
		0533 0533	925 926	:	SHOBRK		
		0530 0533 0533 05333 0533 05336 0536 053	927		JAGUAR		
55 01 58 FB7D CF45	DQ	0533 0533	929	SHUBKK:	MOVL	#1,R5	INIT INDEX FOR LOOP
58 FB7D CF45	DQ 13	0536 0530	930 931	10\$:	MOVL Beql	BRKADR[R5],R8	GET BREAKPOINT ADDRESS
2E 53 55	ġŎ	053E	932		MOVL	R5,R3	INIT INDEX FOR LOOP GET BREAKPOINT ADDRESS SKIP IF NULL BREAKPOINT NUMBER NEW LINE BPT NUMBER
FE70 FE16	90 30 30 30	0544	933		B2BM B2BM	CRLF OUTDIGIT OUTSPACE	NEW LINE BPT NUMBER
FĒ6Š 53 58	30 00	0547 054A	935	\$HOBRK:	MOVL BSBW BSBW MOVL BSBW MOVL BEQL BSBW	UUISPALE	SPACE ADDRESS OF BPT
FE11 F75C	30 30	054D	936 937 938 939		BSBW	R8 R3 OUTLONG	UUIFUI MUUNESS
53 F888 C£45	50 D0	0550 0553	938 939		MOVL BSBW	OUTSPACE BRKDSPER5],R3	SPACE OVER GET DISPLAY START
03 FE03	DO 13 30	0559 055B	940 941		BEQL	155 ;	NONE
7203	JU	סננט	741		D 3 D#	OUTLONG ;	OUTPUT DISPLAY START

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		057F 962		.SBTTL	SEMI-I, PC VALUE	
		057F 962 057F 963 057F 964 057F 965	;	SEMI-I		
FC9D	30	057F 965 057F 966	COLON:	BSBW	ENDEXPR	TERMINATE EXPRESSION
F8 AB 57	DO	0582 967	0000	MOVL	R7,PID-B(R11)	TERMINATE EXPRESSION SET PID FOR PROCESS
70	7 C 0 S	0586 968 0588 969 0589 970		CLRQ RSB	R6	RESET ACCUMULATORS
51 F4 AB	DE	0589 970 0589 971	MFYFLGS	:MOVAL	MFYFLG-B(R11),R1	SET MODIFY FLAG ADDRESS
17	DE 11	058D 972	DOT:	BRB MOVAL	VALUE CURDOT-B(R11),R1	SET/GET VALUE SET ADDRESS OF DOT WAS IT PROCESSOR REGISTER? YES, SET PROCESSOR REGISTER MODE
18 6A 1F	DE E1 E2	0592 974	DO1.	BBC	V PREG (R10) VALR	WAS IT PROCESSOR REGISTER?
14 6A OF 12	11	0596 975 059 A 976		BBSS BRB	#V_PREG.(R10),VALR #V_PRMODE,(R10),VALR VALR	; YES, SET PROCESSOR REGISTER MODE ; READ VALUE
51 04 AB 0C	DE 11	059C 977 05A0 978	QUANT:	MOVAL BRB	QUAN-B(R11),R1 VALR	SET QUANTITY ADDRESS READ VALUE
		05A2 979	PROGETR	:		
51 54 AB 04 6A 08	DE E1	05A2 980 05A6 981	VALUE:	MOVAL BBC	SAVPC-B(R11),R1 #V F1,(R10),VALR	; SET PC ADDRESS : SKIP IF NO VALUE
61 E0 AB 56 61	D0 D0	05AA 982	VALR:	MOVL MOVL	#V F1,(R10), VALR F1=B(R11),(R1) (R1),R6; AND GET VALUE	SKIP IF NO VALUE SET NEW VALUE FOR PC
FC 2	31	05B1 984	VALI:	BRW	INFLD	SET FIELD IN PROGRESS
55 18 AB	DE	05B4 986	REGISTE	MOVAL	SAVREG-B(R11),R5	SET BASE OF REGISTER AREA
02 F5	10 11	05B8 987 05BA 988		BSBB BRB	REGCOM VALI	FETCH ADDRESS AND USE IT
FDFF	30 3A	05BC 989	REGCOM:	BSBW	GETCHAR	: GET SECOND CHAR
FBA3 CF 10 58)A	05BF 990 05C5 991		LOCC .IF	R8,#16,PRIMARY Df,SW_PROCESS	TRANSLATE TO HEX FOR PROCESS VERSION
		05C5 992 05C5 993		BNEQ CMPW	10\$ #^A/XI/,-2(R9)	LEGAL HEX DIGIT CHECK FOR EXIT COMMAND
		05C5 994 05C5 995		BNEQ	ERR3	NO, ERROR YES EXIT
, -		0505 996		.IFF	EXITCODE	
43	13	05C5 994 05C5 995 05C5 996 05C5 997 05C7 998		BEQL .ENDC	ERR3	ERROR, NOT HEX
50 10 50	۲٦	05C7 999 05C7 1000	105:		PO #16 PO	INVERT
50 10 50 56 6540	DE	05CB 1001		SUBL3 MOVAL	RO,#16,RO (R5)[RO],R6	: ACCUMULATE
	05	05CF 1002 05D0 1003		RSB	•	RETURN
36 6A 09 51 E4 AB 04 00	E1 EF	0500 1004 0504 1005	XSET:	BBC EXTZV	#V_F2,(R10),ERR3	ERROR IF NOT TWO FIELDS GET REGISTER NUMBER
51 FB45 CF41	DE	05DA 1006		MOVAL	#07#4,F2-B(R11),R1 XREGV[R1],R1	· AND COMPUTE REGISTER ADDRESS
(4	11	05E0 1007 05E2 1008	XREG:	BRB	VALUE	PROCESS VALUE X-REGISTER VALUE SET ADDRESS OF REGISTER VECTOR ADDRESS TO R6
55 FB3E CF D3	DE 10	05E2 1008 05E2 1009 05E7 1010		MOVAL BSBB	XREGV,R5 REGCOM	SET ADDRESS OF REGISTER VECTOR
56 66 C3	DO	05E9 1011		MOVL	(R6),R6	: GET VALUE
(3	11	05EC 1012 05EE 1013 05F0 1014		BRB .ALIGN	VALI LONG	AND NOTE INPUT IN FIELD LONGWORD ALIGN EXCEPTION ROUTINES
		05F0 1014	XDFLACV MCHK:	:		ÄČCĖSŠ VIOLATION HANDLER MACHINE CHECK
**	A E	05F0 1016	1101111	. IF	NDF,SW_PROCESS	
5C 16	D5 12	05F0 1017 05F2 1018		TSTL BNEQ	AP ERR3	CHECK FOR SIMULATOR YES, SKIP RESET

XDELTA	00					- EX SEMI	ECUTIV -1, PC	E DEB	UGGER E		F 8	16-SEP- 5-SEP-	-1984 02:0 -1984 02:0)2:16)7:42	VAX/VMS Macro	o V04-00 TA.MAR;1	Page	31 (1)
	0	2'	01	0000000	0 ' GF	8F	05F4 05F4 05F4 05F4 05F4	1019 1020 1021 1022 1023	30012\$:	CPUDISP CASEB	CLR_75	50, - 50>			PATCH ON CPU 80,S^#<<30013	TYPE* \$-30012\$>/2>-1		
						000B'	05FC	1024	30013\$:	.SIGNED .SIGNED .SIGNED	_WORD _WORD _WORD	CLR_78C- CLR_750- CLR_730-	-30012\$ -30012\$ -30012\$					
				30	00 03	DA 11	0602 0602 0602 0602 0607	1025 1026 1027 1028	CLR_780	: MTPR BRB	#O,#PF	R\$_SBIFS	•	CLEA	11/780: R SBI FAULT R CLEARED			
				26	0F	DA	0607 0607 060A 060A 060A	1024 1025 1026 1027 1028 1029 1031 1032 1033	CLR_730 CLR_750	: MTPR	#^XF,4	VPR\$_MCESR		FOR	11/730: 11/750: 1 TO CLEAR MC	HECK ERROR SUMM DENT CODE*	ARY	
				1	FB91	31	060A 060A 060A 060A	1034 1035 1036 1037 1038 1039	10\$: ERR3:	.ENDC BRW	ERROR		•	AND	DECLARE ERROR			

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								FITTE SINGENESSING IN THE I
				060D 060D	1041 1042 1043 ;	.SBTTL	REGISTER SAVE AND RESTORE	:
				060D 060D	1044 ; 1045 :	SAVE -	SAVE TARGET PEGISTERS, PC.	, PSL
	12	1 F	DA	060D 060D 060D	1046 SAVE: 1047 1048	.IF SETIPL	MTDD #21 CA#DD& 1Di	DISABLE
				0610	1049 ;	JSB	MTPR #31,S^#PR\$_IPL INI\$WRITABLE	: MAKE THE SYSTEM WRITABLE
FA5B	CF	50	7D 9E	0610	1050	MOVQ	RO, SAVREG	SAVE RO,R1
51	FA5F	Cr	YE	0615 061A	1052	MOVAB .IFF	SAVRZ,R1	; SETUP BASE FOR REMAINING REGS
				061A	1051 1052 1053	SSETAST	_s #0	DISABLE ASTS
				061A	1054	PUSHAB	-(RO)	SAVE ENABLE VALUE-1
				061A 061A	1055 1056	MOVPSL EXTZV	MPSI ZV CIBMOD MPSI ZS CUB	; GET CURRENT PSC MOD P1 P1
				061A	1057	MÛLW	#CONTEXTSZ,R1	MAKE THE SYSTEM WRITABLE SAVE RO,R1 SETUP BASE FOR REMAINING REGS FALSE IF PROCESS VERSION DISABLE ASTS SAVE ENABLE VALUE-1 GET CURRENT PSL MOD,R1,R1 COMPUTE OFFSET TO PROPER CONTEXT AREA FORM ADDRESS OF REGISTER SAVE GET POINTER TO MECHANISM SAVE RO,R1
				061A 061A	1058 1059	MOVAB	SAVREG[R1],R1	FORM ADDRESS OF REGISTER SAVE
				061A	1060	MOVL MOVQ	12(R0),(R1)+	SAVE ROLR1
	•			061A	1061			
	81 81	52 54 56 58	70 70	061A 061D	1062 1063	MOVQ MOVQ	R2,(R1)+ R4,(R1)+	; SAVE R2,R3
	81	56	7D	0620	1064	MOVQ	R6.(R1)+	SAVE RO.R7
	81	58	7D	0623	1065	MOVQ	R8, (R1)+	SAVE R8,R9 SAVE R10,R11
	81	5A	7D	0626 0629	1066 1067	MOVQ .If	R6,(R1)+ R8,(R1)+ R10,(R1)+ NDF,SW_PROCESS AP,(R1)+ 12(SP),(R1)+	SAVE R10,R11
	81	5 C	7D	0629	1068	MOVQ	AP, (R1)+	SAVE AP, FP
81 81	0C 04	AE	9E	0620	1069	MOVAB	12(SP),(R1)+	; ASSUME KERNEL STACK
01	04	AE	7D	0634	1070 1071	MOVQ .Iff	4(SP),(R1)+	SAVE PC,PSL
				062C 0630 0634 0634	1072	MOVQ	8(FP),(R1)+	SAVE AP, FP
				0634 0634	1073	SUBL 3	#1, 3 4(AP),R0 ;	: GET NUMBER OF ARGS IN SIGNAL
				0634	1074 1075	MOVAL MOVAL	a4(AP)[RO],RO 8(RO),(R1)+	POINT TO PC,PSL COMPUTE SP
				0634	1076	MOVQ	(RO),(R1)+	SAVE PC, PSL
				0634 0634	1077 1078	.ENDC .IF	NUE ON BRUCECO	
				0634	1079	MFPR	NDF,SW_PROCESS #PR\$_TXCS,(R1)+	SAVE CONSOLE TRANSMIT STATUS
	81	22	DB	0634			MFPR #PR\$_TXCS,(R1)+	
	81	20	DB	0637 0637	1080	MFPR	<pre>MPRS_RXCS,(R1)+ MFPR</pre>	SAVE CONSOLE RECVR STATUS
	•	20 50	D4	063A	1081	CLRL	AP ;	ZAP DEVICE ADDRESS BASE
				063C	1082	.ENDC	NOT OU DOCCECE	
	22	00	DA	063C 063C	1083 1084	.IF MTPR	NDF,SW_PROCESS : #0.#PR\$_TXCS :	CLEAR INTERRUPT ENABLE
	50 55	ŎŎ	DA	063F	1085	MTPR	#O, #PR\$ RXCS	FOR BOTH TRANSMIT AND RECEIVE
				0642	1086	.ENDC		
5B	FA12	CF	9E	0642 0642	1087 1088 20\$:	.IF MOVAB	NDF,SW_PROCESS B,R11	AND DATA BASE ADDRESS
- -		-		0647	1089	.IFF		FALSE FOR PROCESS VERSION
				0647 0647	1090 1091	MOVAB MOVL	W^ <b-<savpsl+4>>(R1),R11 (SP)+,ASTEN-B(R11) ;</b-<savpsl+4>	; SET BASE OF CONTEXT AREA SAVE AST ENABLE
				0647	1092 1093	.ENDC	COT / T, MOTER TO (NTT)	SATE AST ENABLE
5A	DC	AB	9E	0647	1093	MOVAB	STATUS-B(R11),R10	SET STATUS BASE
59	AL	AB	9E	064B	1094	MOVAB	INBUF-B(R11),R9 ;	POINT TO INPUT BUFFER

G 8

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	REGISTER SAVE AND	RESTORE	5-SEP-1984 02:0	7:42 [MP.SRC]XDELTA.MAR;1
69 FBOA CF 04 A0 04 A0 93 AF 20 A0 8E AF 24 A0 89 AF 18 A0 84 AF 50 08 AE 02 18 07 50 00 50 AB 50 FDF7	94 064F 1095 0651 1096 30 0651 1097 D0 0654 1098 9E 065A 1099 9E 065F 1100 9E 0664 1101 9E 0669 1102 EF 066E 1103 13 0674 1104 C0 0676 1105 0679 1106 DB 0679 067D 1107 31 067D 1108 30	.ENDC	:	MAKE BUFFER EMPTY GET BASE OF SCB SAVE ORIGINAL MCHK VECTOR SET TO XDELTA VECTOR SET ACCESS VIOLATION VECTOR SET PG FAULT VECTOR SET RESERVED OPERAND HANDLER OD.8(SP),RO; GET MODE CORRECT ALREADY IF KERNEL COMPUTE PROCESSOR REGISTER AND SAVE CORRECT SP
04 AE 54 AB	0680 1109 0680 1110 ; 0680 1111 ; 0680 1112 ; 0680 1113 RE 0680 1114 7D 0685 1116 0685 1117 0685 1118 0685 1119 0685 1120 0685 1120	RESTORE IF MOVQ IFF SUBL3 MOVAL MOVQ .ENDC	- RESTORE TARGET REGISTER NDF, SW_PROCESS SAVPC-B(R11),4(SP) #1,04(AP),R0 04(AP)[R0],R0 SAVPC-B(R11),(R0)	
20 A0 00000000 EF 24 A0 00000000 EF 04 A0 FAC9 CF 18 A0 00000000 EF 50 OA 22 5C AB 20 60 AB 09 04 AC 5C AB 6C 5E AB	B0 06B8 1134 06BC 1135	MOVAB MOVAB MOVAB TSTW BNEQ MTPR MTPR BRB OS: MOVW	SAVOCR-B(R11), OUTCR(AP); SAVRCR-B(R11), RDCR(AP);	AND PAGE FAULT VECTOR RESTORE MACHINE CHECK VECTOR RESTORE RESERVED OPERAND VECTOR CHECK FOR CONSOLE NO, OTHER DEVICE RESTORE INITIAL TX STATUS AND INITIAL RECEIVER STATE MERGE WITH COMMON CODE RESTORE OUTPUT CSR AND INPUT CSR CONTENT
51 20 AB 52 81 54 81 56 81 58 81 5A 81 50 F99A CF	06BC 1136 06BC 1137	PUSHL .ENDC 0\$: MOVAB MOVQ MOVQ MOVQ MOVQ MOVQ	ASTEN-B(R11) SAVR2-B(R11),R1 (R1)+,R2 (R1)+,R4 (R1)+,R6 (R1)+,R8 (R1)+,R10 NDF,SW_PROCESS (R1)+,AP SAVREG,R0 (R1)+,8(FP) 8(AP),R0 <savreg-savsp>(R1),12(R0)</savreg-savsp>	SAVE AST ENABLE SET BASE FOR RESTORE RESTORE R2,R3 RESTORE R4,R5 RESTORE R6,R7 RESTORE R8,R9 RESTORE R10,R11 RESTORE AP,FP RESTORE RO,R1 FALSE IF PROCESS VERSION SET NEW VALUES FOR AP,FP GET MECHANISM POINTER ; STORE UPDATED R0,R1

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06D7 1151 06D7 1152 06D7 1153 06D7 1154 30\$: 06D7 1155 06D7 1156 06D7 1157 06D7 1158 35\$: 06D7 1159 MOVPSL EXTZV BBCC #PSL\$V_CURMOD_#PSL\$S_CURMOD_R1,R1 ; GET_CURRENT_MODE R1,DBGACTIVE,30\$; CLEAR ACTIVE BIT FOR MODE TSTL (SP)+
BEQL 35\$
\$SETAST_S CHECK FOR AST ENABLE NO RE- ENABLE AST RECOGNITION #1 .ENDC 06D7 1160 06D7 1161; 06D7 1162 06D7 1163 NDF SW PROCESS INI SRDONLY JSB : REPROTECT THE SYSTEM CODE .ENDC

1 8

RSB

: AND RETURN

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-	EX	Ε	CUT	IVE	DEBUGGER
				ADDE	

16-SEP-1984 02:02:16 VAX/VMS Macro V04-00 5-SEP-1984 02:07:42 [MP.SRC]XDELTA.MAR;1

			06D8 06D8	1166 1167	.SBTTL	GET SCB ADDRESS	
			06D8 06D8 06D8 06D8	1168 ; 1169 : SUBRO	UTINE GET	TSCB IS CALLED TO GET THE E CURRENT SCB.	PHYSICAL OR VIRTUAL
			0608	1172 : INPUT	S :	NONE	
			06D8 06D8 06D8 06D8 06D8	1173 1174 : OUTPU 1175 1176 :		RO = SCB ADDRESS OTHER REGISTERS PRESERVE	D
	50 38	N.A.	06D8	1177 1178 1179 GETSCB:	.IF MFPR	NDF,SW_PROCESS #PR\$_MAPEN,RO MFPR #PR\$_MAPEN.RO	: NOT FOR PROCESS VERSION : GET MAPPING STATUS
	50 38 05	DB 12	06DB	1180	BNEQ	10\$; BRANCH IF MAPPING ENABLED
	50 11	DB	06DD	1181	MFPR	MPR\$_SCBB,RO MFPR MPR\$_SCBB,RO	; ELSE GET PHY ADDR OF SCB
50	00000000 EF	11 DE 05	06E0 06E2 06E9 06EA	1182 1183 10\$: 1184 20\$: 1185	BRB MOVAL RSB .ENDC	20\$ SCB\$AL_BASE,RO	; JOIN COMMON RETURN ; IF MAPPING ENABLED, GET SCB VA ; RETURN ;

DF,SW_PROCESS

#2, as AVPC-B(R11)

#V_TBIT,(R10),50\$; TEST AND CLR TRAG #PSL\$V_TBIT,SAVPSL-B(R11),40\$; SET TBIT

TEST AND CLR TRACE FLAG

FOR PROCESS VERSION

CHECK FOR REI OPCODE NO, NOTHING SPECIAL

XDELTA

09 6A

00 58 AB

03

E5

Ē2

0753

0757

075C

075C

305:

BBCC

BBSS

. IF

CMPB

BNEQ

	- EXECUTI	IVE DEBUGGER HANDLER		L 8 16-SEP-1984 02 5-SEP-1984 02	:02:16 VAX/VMS Macro V04-00 Pag :07:42 [MP.SRC]XDELTA.MAR;1	e 37 (1)
00 6A 05 FF1D	0750 0750 0750 0750 0760 0763 02 0763 0764 0764	1246 1247 45\$: 1248 50\$: 1249 1250 1251 1252 1253	EXTZV MULW MOVAB .ENDC BBSS BSBW .IF REI .IFF MOVL RET .ENDC	#PSL\$V_CURMOD,#PSL\$S_CU #CONTEXTSZ_RO STATUS-B(RÓ),R10 #V_TBITOK,(R10),50\$ RESTORE NDF,SW_PROCESS #1,R0	RMOD SAVPSL-B(R11) RO GET NEW MODE SCALE BY PER MODE CONTEXT AREA SIZE POINT TO NEW FLAGS SET TBIT EXPECTED RESTORE EVERYTHING AND RETURN FALSE IF PROCESS VERSION RETURN TRUE	

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0764	1257 .SBT1	L TBIT EXCEPTION HANDLER	
0764 0764	1259 HANDL	ER FOR TBIT EXCEPTION	
0764 0764 0764 0764 0764 0764	1257 .SBT1 1258 ; 1259 ; HANDL 1260 ; 1261 .ALIG 1262 .IF	N LONG ; LON	GWORD ALIGNED
0764 0764	1263 XDELTBIT	NDF,SW_PROCESS XDE	LTA TBIT HANDLER
U/04	1264 . IFF 1265 XDELTBIT:		
0764 FEA6 30 0765	1266 .END(1267 BSBW	SAVE : SAV	E AND DISABLE
10 6A 05 E4 0767 FF17 30 076B	1268 BBSC 1269 BSBW	WV_TBITOK,(R10),XDELDBG; BR RESTORR; RES	IF TBIT EXPECTED TORE REGISTERS
7E 06 AE 9A 076E	1270 . ÎF 1271 MOVZE 1272 ENBÎN		IPL FOR ENABLE
0772 12 8E DA 0772 00000000'EF 17 0775		MTPR (SP)+,S^#PR\$_IPL	
0778 0778 0778	1273 JMP 1274 . IFF	EXESTBIT ; OTH ; FAL RO : RES	ERWISE LET EXEC HANDLE SE IF PROCESS VERSION
0778 0778 0778	1273 JMP 1274 .IFF 1275 CLRL 1276 RET 1277 .ENDO 1278 XDELDBG: 1279 BICL 1280 BSBB	; UNE	IGNAL XPECTED TBIT EXCEPTION
077B	1277 .ENDC 1278 XDELDBG:	; (OM	MON WITH DEBUG EXCEPTION
14 10 077F	1279 BICL 1280 BSBB	COM #<1@PSL\$V_TBIT>,SAVPSL-B(R11) UNBRK REP #V_ATBRK,(R10),PROCEED CHE	LACE OPCODES
0785	1282;		CK FUR PRUCEED
0785 0785	1284 ;	T STEP MESSAGE	
6B 54 AB DO 0785 0789	1285 MOVL 1286 IFNOR	SAVPC-B(R11), CURDOT-B(R11) D #4, acurdot-B(R11), GETCMD	; SET ADDRESS ; SKIP DISPLAY IF NOT READABLE
00 BB 04 00 0C 0789 BC 13 078E		SAVPC-B(R11), CURDOT-B(R11) D #4, aCURDOT-B(R11), GETCMD PROBER #0, #4, aCURDOT-B(R11) BEQL GETCMD	
FB64 30 0790 B7 11 0793	1287 BSBW 1288 BRB	LUCPKUMPI ; PRUI	MPT WITH ADDRESS/CONTENT GET COMMANDS
0795	1289		

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		- EXE	ECUTIVE K - RES	DEBU	JGGER OPCODES	FOR BRE		8 NTS	16-SEP- 5-SEP-	-1984 -1984	02:02:16 02:07:42	VAX/VMS Ma	ecro VO4-00 DELTA.MAR;1	Page
			0795 0795 0795 0795 0795	1291 1292 1293 1294		.SBTTL UNBRK	UNBR	K - F	RESTORE	OPCOD	ES FOR B	REAKPOINTS		
50	51 08 F91B CF41 06	D0 D0 13	0795 0795 0798 079E 07A0	1291 1293 1293 1296 1296 1298 1299 1300	UNBRK: 10\$:	MOVL MOVL BEQL .IF	20\$	DR[R1	1],R0		; GET	T LOOP BREAKPOINT P IF NOT ENA	ADDRESS ABLED	
			07A0	1300 1301 1302 1303 1304		PÜSHR MOVL MOVL BSBW	NºM< RO,R RO,R SETW	4	OCESS 1,R2,R3,	,R4 ,R5	; FOR	E REGS FOR F M INADR RANG PAGE WRITAE	GE FOR SET P	CHANGE PROTECTION
60	F936 CF41	90	07A0 07A0 07A0 07A6	1305 1306 1307		MOVQ .ENDC MOVB .IF	(SP) BRKO	,R0 P[R1]	(RO) CESS		RES	TORE ROURT	,,,,	
	EF 51	F5	07A6 07A6	1308 1309 1310 1311	20\$:	BSBW POPR .ENDC SOBGTR	REPRI	RO,R1	1,R2,R3,	,R4,R5	> ; RES	TORE PROTECT TORE REGISTE THEM ALL	[ION ERS	
		05	07A9	1312 1313		RSB	·					RETURN		

XDELTA VO4-000

	- EX SETB	ECUTIV	E DEBUGGER ET BREAK POIN	T INSTRU	B 9 16-SEP-1984 02: ICTIONS 5-SEP-1984 02:	02:16 VAX/VMS Macro V04-00 Pag 07:42 [MP.SRC]XDELTA.MAR;1	e 40
		07AA 07AA 07AA 07AA	1315 1316 : 1317 :	.SBTTL SETBRK	SETBRK - SET BREAK POINT	INSTRUCTIONS	
51 08 50 F906 CF41 14 F920 CF41 60	D0 D0 13	07AA 07AD 07B3	1319 SETBRK: 1320 10\$: 1321	MOVL MOVL BEQL MOVB	#NBRK,R1 BRKADR[R1],R0 20\$ (R0),BRKOP[R1]	: INIT LOOP : GET ADDRESS : SKIP IF NOT ENABLED	
6A 18 06 54 AB 50 03	90 93 13 01 13	07B5 07BB 07BE 07C0 07C4	1316; 1317; 1318; 1319 SETBRK: 1320 10\$: 1321 1322 1323 1324 1325 1326 1327 15\$:	BITB BEQL CMPL BEQL	#<<1av_tBRND-LR13 #<<1av_tBIT>!<1av_ATBRK> 15\$ R0,SAVPC-B(R11) 20\$; SAVE OPCODE >,(R10); CHECK FOR TRACE ; NO TRACE, SET ANYWAY ; CHECK FOR AT BPT ; YES, DONT SET IT	
		07C6 07C6 07C6	1328	.IF PUSHR MOVL	DF,SW_PROCESS #^M <ro,r1,r2,r3,r4,r5> R0,R4</ro,r1,r2,r3,r4,r5>	: SAVE REGISTERS FOR PROTECTION CHANGE : SET START ADDRESS OF RANGE	
		07C6 07C6 07C6 07C6	1330 1331 1332 1333 1334 1335	MOVL BSBW MOVL .ENDC	RO,R5 SETWRT (SP),RO	: AND END ADDRESS :SET PAGE WRITABLE : RESTORE BPT ADDRESS	
60 03	90	07C6 07C9 07C9 07C9	1335 1336 1337 1338 1339	MOVB .If BSBW POPR	#3,(R0) DF,SW_PROCESS REPROT #^M <r0,r1,r2,r3,r4,r5></r0,r1,r2,r3,r4,r5>	RESTORE ORIGINAL PROTECTION VALUE AND REGISTERS	
E1 51	f 5 05	07C9 07C9 07CC 07CD	1339 1340 20 \$: 1341 1342	.ENDC SOBGTR RSB	R1,10\$	DO THEM ALL AND RETURN	

XDELTA VO4-000

XDE

Syn

RSB

RETURN

XDELTA VO4-000

XDE

Syn

PR!

PROCESSOR REGISTER

DEPOSIT IN PROCESSOR REGISTER

DEPOSIT IN ARBITRARY PROCESS

; SWITCH ON TYPE

0817 0817

0817

0817

0817

0817

0817

1414 405:

50\$:

SCMKRNL_S

60\$,-

RSB

CASE

B^DEPPREG_(AP)

CURTYPE-B(R11), TYPE=B,<-

1415

1416

1418

1419

Pse

XDE

PSE

SAE

7\$[

Pha ---Ini COM Pas Sym Pas Syn

Pse Crc ASS The

872 The 184

Mac ----\$2 -\$2 -\$2 TO1 139

The MA(BEQL

ADDL RSB

MOVAB

MTPR

MOVL

. WORD

ADDL3

MOVAB

MOVZWL

MOVL

RET

.ENDC

RET

1438 1439 DEPPREG:.WORD

SCMKRNL_S

100\$

#20,SP

#1,R0

#1,R0

#4,8(AP),R1 (R1),12(FP) B^10\$,16(FP)

W^QGET, (RO)

W^PREXC, (FP) F1-B(R11), CURDOT-B(R11)

0817

0817

0817 0817

0817

0817

0817

0817

0817

0817

0817 1440

0817 1441

0817 1446

0817 1447

0817 1448

1434

1435

0817 1436 100\$: 0817 1437

1442

1444 0817 1445 PREXC:

0817 1449 10\$:

1450

1452

1451

WORD LONGWORD

SET ADDRESS OF BYTE ROUTINE SET ADDRESS OF WORD ROUTINE 44 (1)

Page

SET ADDRESS OF LONG ROUTINE SET PID OF TARGET ADDRESS FOR STORE VALUE TO STORE ARGUMENT COUNT POINTER TO ARGUMENT LIST CHECK FOR STORE ENABLED BR IF NOT CALL TO QUEUE REQUEST CLEAN STACK

AND RETURN : DEPOSIT INTO PROCESSOR REGISTER SET EXCEPTION HANDLER

PLACE FIELD VALUE IN REG RETURN SUCESS

PROCESSOR REGISTER EXCEPTION HANDLER

POINT TO EXCEPTION FP SET AS RETURN FP SET RETURN ADDRESS

SET NORMAL STATUS ; AND RETURN

**

TRELITA

P EXECUTIVE DEBUGGER

N 9

16-SEP-1084 02:07:12 VAXYVMS Macro v04-00

P EXPROCESSOR REGISTER PREFIX

0825 1466 ... SBTTL P - PROCESSOR REGISTER PREFIX

0825 1466 ... SET PROCESSOR REGISTER MODE

00 6A 0F 62 0825 1470 PREG:

00 6A 0F 62 0825 1472 10\$: RSB

#V_PRMODE.(R10).10\$:: PROCESSOR REGISTER MODE

1 PROCESSOR REGISTER MODE

1 PROCESSOR REGISTER MODE

2 RETUPN

2 RETUPN

2 RETUPN

3 PROCESSOR REGISTER MODE

3 RETUPN

4 PROCESSOR REGISTER MODE

5 RETUPN

5 PROCESSOR REGISTER MODE

6 RETUPN

6 PROCESSOR REGISTER MODE

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7 Pa

EXE

Moc

MS(SYS

```
082A 1474
082A 1475
082A 1476
                         .SBTTL PROCESS DEBUGGER INITIALIZATION
      1476 .IF DF.SW_PROCESS
1477 SALUTE: .ASCIZ <CR><[F>/DELTA Version X2.1/<CR><LF>
082A 1478
      1479 TEST:
                                                                   ; START ADDRESS OF IMAGE ENTRY
082A 1480 XDT$START::
                                                                   ; GLOBAL START ADDRESS FOR CLI DEBUG
      1481
                         .WORD 0
      1482 DELTA START:
1483 SWAKE
                                                                  ; START ADDRESS FOR DEBUGGER ENTRY
                        SWAKE S
                                                                   : NULL WAKE AND
                        SHIBER S
                                                                  ; HIBERNATE TO GET SYNCHRONIZED
; RELOCATE TERMINATOR MASK DESCR
; RELOCATE DESCRIPTOR
      1484
                                  TERMASK, TERMASKD+4
TTSTR, TTNAMD+4
      1485
                         MOVAB
       1486
                         MOVAB
       1487
                                   EXIHANDLE, EXIHADR
                         MOVAB
       1488
                                   EXITCODE, EXCODA
                                                                     RELOCATE EXIT HANDLER ARGS GENERATE CALL FRAME
                         MOVAB
       1489
                                   (AP),B^INITCALL
                         CALLG
       1490
                         RET
       1491
      1492 NOBRK:
1493
                                                                  ; GET EXCEPTION ARGUMENT LIST ; AND GOTO EXCEPTION HANDLER
                                   4(AP),AP
EXCEPT+2
                        MOVL
                         BRW
082A
      1494
      1495 INITCALL:
                         .WORD O MOVAB W^CATCHALL,(FP)
                                                                  ; ENTRY MASK
A$80
      1496
                                                                  ; SET CATCHALL EXCEPTION HANDLER ; DECLARE USER MODE EXIT HANDLER
082A
      1497
                        SDCLEXH_S
SCMKRNL_S
SSETEXV_S
082A
      1498
                                             EXITBLK
                                             W^SETEXC, (AP);
ADDRES=W^EXCEPT,-
                                                                  SET EXCEPTION VECTORS
      1499
       1500
                                             ACMODE=#3,- ;
AS80
      1501
                                             VECTOR=#0 : SET PRIMARY FOR USER

ADDRES=W^CATCHALL, - : SET LAST CHANCE HANDLER

ACMODE=#3, - : FOR USER MODE

VECTOR=#2 : SPECIFY LAST CHANCE HANDLER
      1502
1503
                        $SETEXV_S
082A
082A
      1504
082A
      1505
                        $ASSIGN_S
BLBS RO,10$
                                                                ASSIGN DEVICE
                                           TTNAMD, TTCHAN
AS80
      1506
                                                                     CONTINUE IF SUCCESS
ELSE EXIT WITH ERROR CODE IN RO
SET ADDRESS OF SALUTATION
AS80
      1507
082A
      1508
                         RET
                                  SALUTE, R4
OUTZSTRING
082A 1509 10$:
                         MOVAB
AS80
      1510
                         BSBW
                                                                     OUTPUT IT
                                                                 NOBRK ; BR IF LATER INVOCATION ; VIA SDEBUG COMMAND
                                   #CLI$V_DBGEXCP,24(AP),NOBRK
082A
      1511
                         BBS
      1512
1513
082A
                                                                  : CREATE TOP CALL FRAME
082A
                         CALLG (AP),B<sup>2</sup>0$
082A
      1514
                         RET
                                                                 NULL ENTRY MASK
      1515 20$:
                         . WORD
                                                                 ; ADVANCE STARTING ADDRESS POINTER
                                  #4,4(AP)
-($P)
       1516
                         ADDL
                                                                 SAVE PSL

: FETCH CURRENT STARTING ADDRESS

: SET EXCEPTION CODE
       1517
1518
                        MOVPSL
ADDL3
                                   #2,24(AP),-(SP)
       1519
1520
1521
1522
1523
1524
1525
1526
1527
1528
1529
                                  #SS$_DEBUG,-(SP)
A580
                         MOVZWL
AS80
                         PUSHL
                                                                     SIGNAL ARG COUNT
                                                                     SAVE POINTER
SAVE PHONY RO,R1
082A
                         MOVL
                                   SP,RO
                         MOVQ
                                   RO,-(SP)
AS80
AS80
                         PUSHL
                                   #0
                                                                     DEPTH
AS80
                         PUSHL
                                                                  ARG COUNT POINTER TO MECH
082A
                         PUSHL
                                   #4
082A
                         PUSHL
                                   SP
                         PUSHL
                                                                  ; POINTER TO SIGNAL
AS80
                                  #2,W^EXCEPT
#12,SP
                                                                 ; SIGNAL PHONY EXCEPTION
082A
                         CALLS
                                                                 CLEAN BACK TO RO,R1
RESTORE RO,R1
082A
                         ADDL
                                   (SP)+_RO
       1530
                         MOVQ
```

DEF

- EXECUTIVE DEBUGGER

_\$?

Psi

MS

_\$2

```
082A 1588
082A 1589 60$:
082A 1590
082A 1591
                                                               : IGNORE AND RESIGNAL
                       MOVL
                                 #1,R0
                       RET
                       .PAGE
082A 1592
                       .SBTTL HANDLER FOR DEBUG EXCEPTIONS
082A 1593
ĎŘŽA 1594 DBGEXCEP:
082A 1595
                        . WORD
                                                           POINT TO EXCEPTION FP
INIT LINK FOR CALL FRAMES
IS THIS THE LAST ONE?
                                 #4,8(AP),R1
082A 1596
                       ADDL3
082A 1597
                       MOVL
                                 FP_RO
082A 1598 10$:
                                 12(RO),(R1)
                       CMPL
082A 1599
082A 1600
                       BEQL
                                 20$
                                                                 YES
                                 B~30$,16(RO)
                                                               ; SET FOR RETURN
                       MOVAB
082A 1601
                                 12(RO),RO
                       MOVL
082A 1602
082A 1603 20$:
082A 1604 30$:
                       BRB
                                                              CONTINUE SET RETURN FOR ERROR
                                 10$
                       MOVAB
                                 XDELACV, 16(RO)
                       RET
082A 1605
082A 1606 CATCHALL:
                                                               ; CATCHALL EXCEFTION HANDLER
082A 1607
                       .WORD
                                                                ENTRY MASK
082A 1608
                       MOVPSL R1
                                                                GET CURMOD
                                 #PSL$V_CURMOD.#PSL$S_CURMOD.R1.R1 ; ISOLATE CURRENT R1.DBGACTIVE.10$ ; MUST NOT BE DEBUGGER EXCEPTION
082A 1609
                       EXTZV
                                                                                  ; ISOLATE CURRENT MODE
082A 1610
                       BBCS
082A 1611
                       CLRL
                                                               ; RESIGNAL
082A 1612
082A 1613 10$:
                       RET
                                                          SAVE EVERYTHING
POINT TO EXCEPTION CODE
GET IT
OUTPUT CR/LF
                       BSBW
082A 1614
                                 #4,4(AP),RO
                       ADDL3
                                 (RÖ),R3
082A 1615
                       MOVL
082A 1616
                                 CRLF
                       BSBW
                                                             : OUTPUT EXCEPTION CODE
082A 1617
                       BSBW
                                 OUTLONG
                                                             ; OUTPUT MESSAGE
082A 1618
                       MOVAB
                                 B^EXCMSG_R4
082A 1619
                       BSBW
                                 OUTZSTRING
                                                             ; TEXT FOR EXCEPTION
082A 1620
                                                              ; AND DISPLAY INSTRUCTION
                       BRW
                                 XDELDBG
082A 1621 EXCMSG: .ASCIZ / EXCEPTION /
082A 1622
082A 1623 EXIHANDLE:
                                                           EXIT HANDLER
                       .WORD 0
                      BITB #15,DBGACTIVE
BEQL 10$
$CMKRNL_S CLREXV,(AP)
MOVL 24(AP),RO
RET
082A 1624
082A 1625
                                                            ; TEST FOR DEBUG ACTIVE IN ANY MODE
082A 1626
082A 1627
082A 1628
                                                              : NO, REPORT EXIT
                                                              : RESET EXCEPTION VECTORS
                                                                 RESTORE
082A 1629
082A 1630 10$:
                                                                 RETURN
                                                                 PROGRAM EXIT
082A 1630
082A 1631
082A 1633
082A 1633
082A 1635
082A 1636
082A 1637
082A 1638
082A 1639
082A 1640
082A 1641
082A 1643
082A 1643
                       MOVPSL -(SP)
                                                                 BUILD EXCEPTION FRAME
                       PUSHL
                                 16(FP)
                       PUSHL
                                 24(AP)
                                                                 EXIT CODE FOR EXCEPTION CODE
                                                              ; EXIT CODE
; ARG COUNT
                       PUSHL
                                 #3
                       PUSHR #^M<RO,R1>
                       MOVQ
                                 AP,-(SP)
                                                              MECHANISM COUNT
POINTER TO MECHAN
POINTER TO SIGNAL
                       PUSHL
                                 #4
                       PUSHL SP
PUSHAL 24(SP)
                                                                POINTER TO MECHANISM
                       PUSHL
                                 112
                                                              SET AP FOR EXCEPTION SAVE EVERYTHING
                                 SP.AP
                       MOVL
                       BSBW
                                 SAVE
                                                              : DISPLAY EXIT MESSAGE
                                 B^EXIMSG,R4
                       MOVAB
082A
       1644
                       BSBW
                                 OUTZSTRING
                                                              : OUTPUT TEXT
```

L 9

(1)

```
AS80
AS80
AS80
                             SAVAP-B(R11),R3
                                                          GET POINTER TO EXCEPTION ARGLIST
      1646
                             4(R3).R3
                                                          GET EXIT CODE ADDRESS
                     MOVL
      1647
                                                          DISPLAY IT
                     BSBW
                             OUTLONG
A$80
      1648
                                      EXITBLK
                     SDCLEXH_S
                                                          RE-ESTABLISH EXIT HANDLER
082A
      1649
                     MOVPSL
                                                          GET CURRENT PSL
                             #PSL$V_CURMOD_#PSL$S_CURMOD_R1_R1 : GET CURRENT MODE R1_DBGACTIVE.20$ ; SET DELTA ACTIVE FOR MODE
082A
      1650
                     EXTZV
082A
      1651
                    BBSS
      1652
082A
            20$:
                     BRW
                             XDÉLDBG
082A
      1654
082A
            EXIMSG: .ASCIZ <CR><LF>/ EXIT /
      1655
082A
A$80
      1656
            CLREXV:
                                                          CLR EXCEPTION VECTORS
082A
      1657
                     . WORD
                                                          ENTRY MASK
082A
      1658
                                      ADDRES=akcond,-
                     $SETEXV_S
AS80
      1659
                                      ACMODE=#0
082A
      1660
                                      ADDRES=BECOND,-
                    $SETEXV_S
082A
      1661
                                      ACMODE=#1
      1662
1663
AS80
                                      ADDRES=@SCOND . -
                     $SETEXV_S
082A
                                      ACMODE=#2
082A
      1664
                     RET
082A
      1665
AS80
      1666
                     .PAGE
AS80
      1667
                     .SBTTL SETWRT - SET PAGES WRITABLE
082A
      1668
082A
      1669 SETWRT:
                    MOVAL SCMKRNLS RO,10$
BLBS RO,10$
(R2),B^SETPRTK
082A
      1670
                                                          ADDRESS FOR RETURN OF PROT
                                      BASETPRTK, (R2)
082A
      1671
082A 1672
082A 1673
                                                          CONTINUE IF NO ERROR
082A 1674 10$:
                                                          RESTORE PROTECTION VALUE
082A
      1675
                                                          RETURN
082A 1676
082A
     1677
            SETPRTK: . WORD
                             R5,-(SP)
082A
      1678
                     MOVQ
                                                          INADR. START AND END ADDRESSES
082A
      1679
                     MOVL
                             SP,R1
                                                          ADDRESS OF INADR
AS80
                    $SETPRT_S
                                      INADR=(R1),-
      1680
AS80
      1681
                                      PROT=#PRT$C_UW,-
                                                          WRITABLE BY ALL
                                      ACMODE=#0,-
AS80
      1682
A580
      1683
                                      PRVPRT=(AP)
                                                          ADDRESS AT WHICH TO RETURN PROT
082A
      1684
                             #1,R0
                    MOVL
                                                          ALWAYS SUCCESS
082A
      1685
                     RET
AS80
      1686
AS80
      1687
            REPROT:
                                                          RESTORE PROTECTION
082A
      1688
                     RSB
A$80
      1689
                     . PAGE
082A
      1690
                     SBTTL
                             FETCHP - FETCH DATA FROM ANOTHER PROCESS
082A
      1691 FETCHP: CASE
                             CURTYPE-B(R11), TYPE=B,<-
AS80
      1692
                             105,-
                                                          0 => BYTE
      1693
                                                         1 => WORD
AS80
                              208.-
                             30$>
      1694
                                                         2 => LONG
AS80
      1695
                                                         UNKNOWN
082A
      1696 10$:
082A
                     PUSHAB WAFPBYTE
                                                         SET FOR BYTE FETCH
                     BRB
      1697
                             40$
AS80
ASS0
      1698 205:
                     PUSHAB WAFPWORD
                                                         SET FOR WORD FETCH
A$80
      1699
                     BRB
                             405
AS80
      1700 30$:
                     PUSHAB WAFPLONG
                                                          SET FOR LONGWORD FETCH
                                                        : SET FOR LUNGWURD FEIGH
082A
      1701 40$:
                             PID-B(R11)
                     PUSHL
```

800 800

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```
SET ADDRESS TO RETURN VALUE

AND ADDRESS OF VALUE

ARGUMENT COUNT

SAVE POINTER TO ARG LIST

Q AST FOR DATA FETCH

BR IF FAILED

WAIT FOR DATA TO RETURN

CLEAN STACK

AND RETURN DATA
082A 1702
082A 1703
082A 1704
082A 1705
                               PUSHAB QUAN-B(R11)
                                           CURDOT-B(R11)
                               PUSHL
                              PUSHL
                                           #4
                                           SP,RO
                               MOVL
                              $CMKRNL_S WAGGET, (RO)
BLBC RO,50$
082A 1706
082A 1707
 082A 1708
                              SHIBER_S
                                          S
#20,SP
 082A 1709 50S:
                               ADDL
 082A 1710
                               RSB
082A 1711
                               .PAGE
                              SBTTL QGET - QUEUE AST TO GET DATA FROM ANOTHER PROCESS
082A 1712
082A 1713 ;
082A 1714 :
                              INPUTS: 04(AP) - LOCATION OF DATA
                                           12(AP) - PID OF TARGET PROCESS
16(AP) - CODE SEGMENT POINTER
        1715 :
082A
082A 1716 ;
082A 1717 :
082A 1718;
                             082A 1719
082A 1720
082A 1721
082A 1722
                              .WORD ^M<R2,R3,R4,R5> ; ENTRY MASK
MOVZWL #SS$ NONEXPR,R0 ; ASSUME BAD PIX
CMPW 12(AP),@#SCH$GL_MAXPIX ; CHECK PIX FOR LEGAL PROCESS
082A 1723 QGET:
082A 1724
082A 1725
082A 1726
082A 1727
                                          BGTR
                               MOVZWL
082A 1728
                               MOVAB
082A 1729
082A 1730
                               JSB
                               BLBC
082A 1731
082A 1732
082A 1733
                               MOVL
                               MOVL
                               MOVB
082A 1734
082A 1735
                               MOVAB
                               MOVL
082A 1736
082A 1737
082A 1738
082A 1739
082A 1740
                               MOVL
                               MOVL
                               MOVL
                               PUSHR
                               MOVC3
082A 1741
                               POPR
                              MOVZBL #PRIS TICOM, R2
USB @#SCHSQAST
082A 1742
082A 1743
                                                                                     SET PRIORITY INCREMENT CLASS
                                                                                  ; QUEUE AST FOR TARGET
; RETURN TO ORIGINAL MODE
082A 1744 10$:
                               RET
082A 1745
082A 1746
                               SBTTL FPBYTE - FETCH BYTE FROM PROCESS
WORD 90$-.-2 ; SIZE OF CODE SEGMENT
082A 1747 FPBYTE: .WORD
                             IFNORD #1,afp_addr(r5),10$; BRANCH IF NOT READABLE

MOVB afp_addr(r5), Fp_value(r5); GET value

MOVL fp_drigpid(r5), ACB$L_pid(r5); SET pid for return ast

MOVB #^X80,ACB$B_RMOD(r5); SET for kast again

MOVAB B^20$,ACB$L_kast(r5); SET new ast address

MOVZBL #PRI$_TICOM_r2; SET PRIORITY INCREMENT CLASS

JMP a#SCH$QAST; QUEUE RETURN AST

IFNOWRT #1,afp_retloc(r5),30$; IF NOT WRITABLE THEN SKIP IT

MOVB fp_value(r5),afp_retloc(r5); RETURN value

MOVL ACB$L_PID(r5),R1; GET_PID_FOR_WAKE

SETIPL #IPL$_SYNCH; RAISE TO SYNCH
082A 1748
082A 1749
082A 1750 10$:
082A 1751
082A 1752
082A 1753
082A 1754
082A 1755 20$:
082A 1756
082A 1757 30$:
         1758
082A
```

Vir Sta Ima Ima Nun Nun Nun

_\$2

Nun Nun Nun Nun Ima Mar Est

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23

A 1 LIP CT

BAS

- EXECUTIVE DEBUGGER

082A

PROCESS DEBUGGER INITIALIZATION

**

```
AS80
AS80
AS80
AS80
                                                                      : WAKE PROCESS : LOWER IPL
                                     BASCHSWAKE
                          SETIPL
                                    #IPL$_ASTDEL
        1760
                                     R5.RO
       1761
                          MOVL
                                                                        SET ADDRESS FOR RELEASE
       1762
1763 90$:
                                                                      ; FREE BLOCK AND EXIT
                                     a#EXESDEANONPAGED
                          JMP
                                                                      : END OF CODE SEGMENT
       1764
082A
        1765
                          .PAGE
082A
        1766
                          .SBTTL
                                     DPBYTE - DEPOSIT BYTE TO PROCESS
                                                                       SIZE OF CODE SEGMENT
IF NOT WRITABLE THEN SKIP IT
082A
        1767
              DPBYTE:
                                     90$-.-2
                          .WORD
                                    #1,afp_RETLOC(R5),30$; II
fp_value(R5),afp_RETLOC(R5),
R5_R0
082A
       1768
               20$:
                          IFNOWRT
        1769
082A
                          MOVB
                                                                                 : RETURN VALUE
                                                                        SET ADDRESS FOR RELEASE
082A
        1770
               305:
                          MOVL
082A
        1771
                                     a#EXESDEANONPAGED
                          JMP
                                                                        FREE BLOCK AND EXIT
       1772
082A
               905:
                                                                      : END OF CODE SEGMENT
       1773
082A
       1774
082A
                          .PAGE
        1775
082A
                          .SBTTL
                                     FPWORD - FETCH WORD FROM PROCESS
                                   ; SIZE OF CODE SEGMENT

#2,afp_addr(r5),10$; Branch if not readable

afp_addr(r5),fp_value(r5); Get value

fp_drigpid(r5),Acb$l_pid(r5); Set pid for return ast

#^X80,acb$b_rmod(r5); Set for kast again

B^20$,acb$l_kast(r5); Set for new ast address

#PRI$_ticom,r2; Set priority increment class

a#sch$qast; Queue_return ast
        1776
082A
               FPWORD:
                          .WORD
       1777
AS80
                          IFNORD
082A 1778
                          MOVW
               105:
082A 1779
                          MOVL
082A
      1780
                          MOVB
       1781
082A
                          MOVAB
      1782
                                                                        SET PRIORITY INCREMENT CLASS
082A
                          MOVZBL
      1783
082A
                          JMP
                          IFNOWRT #2, afp RETLOC(R5), 30$ : IF MOVW FP VALUE(R5), afp RETLOC(R5) MOVL ACBSL PID(R5), R1 : GE SETIPL #IPL$ SYNCH : R/
       1784 20$:
                                                                      ; IF NOT WRITABLE THEN SKIP IT
062A
       1785
082A
                                                                                 ; RETURN VALUE
                                                                    GET PID FOR WAKE
      1786 30$:
082A
082A 1787
                                                                        RAISE TO SYNCH
082A 1788
                                     a#SCH$WAKE
                                                                        WAKE PROCESS
                          JSB
                                    WIPLS_ASTDEL R5,R0
082A 1789
                          SETIPL
                                                                        LOWER IPL
082A 1790
                          MOVL
                                                                        SET ADDRESS FOR RELEASE
082A 1791
                          JMP
                                     a#EXESDEANONPAGED
                                                                        FREE BLOCK AND EXIT
082A
      1792 90$:
                                                                        END OF CODE SEGMENT
082A
      1793
082A 1794
                          .PAGE
082A
      1795
                          .SBTTL
                                    DPWORD - DEPOSIT WORD TO PROCESS
                         WORD 90$-.-2
IFNOWRT #2, afp_RETLOC(R5), 30$
       1796 DPWORD:
082A
                                                                        SIZE OF CODE SEGMENT
       1797
                                                                         IF NOT WRITABLE THEN SKIP IT
082A
               20$:
                                    FP_VALUE(R5), aFP_RETLOC(R5)
R5,R0
      1798
AS80
                          MOVW
                                                                                 : RETURN VALUE
      1799
                                                                      ; SET ADDRESS FOR RELEASE
082A
               30$:
                          MOVL
                                                                      FREE BLOCK AND EXIT
082A 1800
                          JMP
                                     a#EXESDEANONPAGED
082A 1801
               905:
      1802
1803
082A
AS80
                          .PAGE
082A 1804
                          .SBTTL
                                    FPLONG - FETCH LONG FROM PROCESS
082A 1805 FPLONG:
                          .WORD
                                     90$-.-2
                                                                      : SIZE OF CODE SEGMENT
                                    #4, afp addr(R5), 10$; BRANCH IF NOT READABLE
afp addr(R5), fp value(R5); GET value
fp drigpid(R5), ACB$L pid(R5); SET pid for return ast
#180, ACB$B_RMOD(R5); SET for KAST AGAIN
#180, ACB$B_RMOD(R5); SET for KAST AGAIN
                          IFNORD
082A 1306
082A 1807
                          MOVL
082A 1808 10$:
                          MOVL
082A 1809
                          MOVB
082A 1810
                                     B^20$,ACB$L_KAST(R5)
                                                                        SET NEW KAST ADDRESS
                          MOVAB
                          CLRL
AS80
       1811
                                                                        NULL PRIO INCR
       1812
1813 20$:
                                                                        QUEUE RETURN AST
AS80
                          JMP
                                     a#SCH$QAST
                          IFNOWRT #4, afp RETLOC(R5), 30$ : IF NOT WRITABLE THOU FP VALUE(R5), afp RETLOC(R5) : RETURN VAMOVL ACB$L_PID(R5), R1 ; GET PID FOR WAKE
AS80
                                                                      : IF NOT WRITABLE THEN SKIP IT
        1814
1815 30$:
082A
                                                                            ; RETURN VALUE
```

NORMAL END STATEMENT WITHOUT START ADDRESS USED TO ASSEMBLE XDELTA FOR EXEC DEBUGGING.

B 10

.END

082A

MSC

; * *

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. .

00000575 R

GO

MS(

\$0

\$C \$C \$C

\$0

SD

SD

\$0 \$0 \$0

\$0 \$0 \$0 \$0

\$C \$C

MSC

SD

SD

\$EEEEEEEEEEEEEE

\$D \$D \$D

SD

SD \$E

SD

SD

SD

SD

	>		
: synopsis !			

Psect

4----

PSECT name Allocation PSECT No. Attributes ABS 00000000 0.) 00 (0.) NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE SABS\$ Ò.) 00000000 01 (1.) NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE 2090.) Z\$DEBUGXDELTA 0000082A ŎŹ (2.) NOPIC USR REL EXE LCL NOSHR RD WRT NOVEC LONG

E 10

Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	38	00:00:00.11	00:00:00.65
Command processing	38 124	00:00:01.02	00:00:04.74
Pass 1	405	00:00:15.16	00:00:44.35
Symbol table sort		00:00:02.05	00:00:03.57
Pass 2	0 338	00:00:05.33	00:00:12.81
Symbol table output	24	00:00:00.23	00:00:00.68
Psect synopsis output		00:00:00.02	00:00:00.02
Cross-reference output	Ŏ	00:00:00.00	00:00:00.00
Assembler run totals	933	00:00:23.92	00:01:06.82

The working set limit was 1950 pages. 87249 bytes (171 pages) of virtual memory were used to buffer the intermediate code. There were 70 pages of symbol table space allocated to hold 1237 non-local and 91 local symbols. 1842 source lines were read in Pass 1, producing 18 object records in Pass 2. 24 pages of virtual memory were used to define 23 macros.

Macro library statistics !

Macro library name Macros defined \$255\$DUA28:[MP.OBJ]MP.MLB;1
\$255\$DUA28:[SYS.OBJ]LIB.MLB;1
\$255\$DUA28:[SYSLIB]STARLET.MLB;2 10 10 28 TOTALS (all libraries)

1396 GETS were required to define 28 macros.

XDELTA

Psect synopsis

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:XDELTA/OBJ=OBJ\$:XDELTA MSRC\$:MPPREFIX/UPDATE=(ENH\$:MPPREFIX)+MSRC\$:XDELTA/UPDATE=(ENH\$:XDELTA)+MSRC\$:END/UPDATE=(ENH\$

0249 AH-BT13A-SE

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